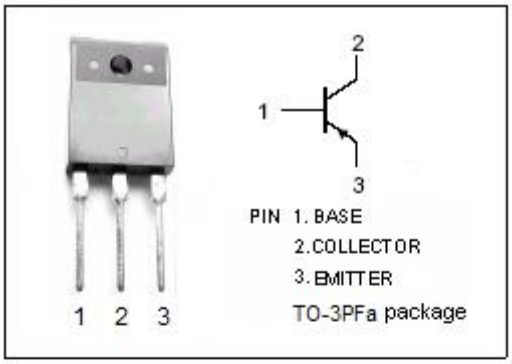
isc Silicon PNP Power Transistor — 2SB1054

DESCRIPTION

- Low Collector Saturation Voltage-
  : $V_{CE(sat)}$= -2.0V(Max)@I_C= -3A
- Wide Area of Safe Operation
- Complement to Type 2SD1485
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

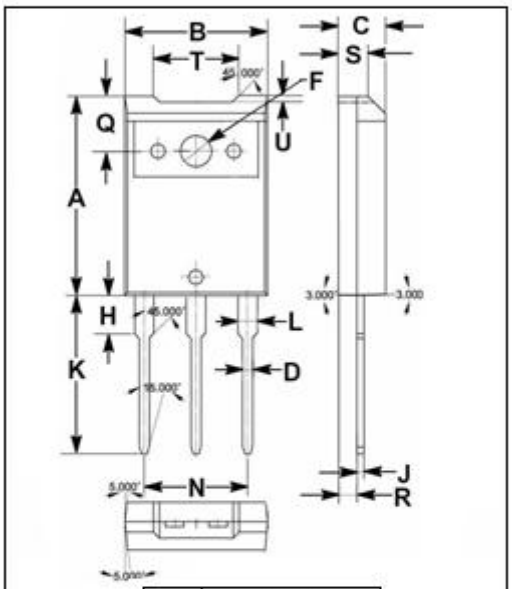


APPLICATIONS

- Designed for high power amplification.

ABSOLUTE MAXIMUM RATINGS(T_a=25℃)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	-100	V
V_{CEO}	Collector-Emitter Voltage	-100	V
V_{EBO}	Emitter-Base Voltage	-5	V
I_C	Collector Current-Continuous	-5	A
I_{CM}	Collector Current-Peak	-8	A
P_C	Collector Power Dissipation @ T_a=25℃	3	W
	Collector Power Dissipation @ T_C=25℃	60	
T_J	Junction Temperature	150	℃
T_{stg}	Storage Temperature Range	-55~150	℃



DIM	mm MIN	mm MAX
A	20.70	21.30
B	14.70	15.30
C	4.80	5.20
D	0.90	1.10
F	3.20	3.40
H	3.70	4.30
J	0.50	0.70
K	16.40	17.00
L	1.90	2.10
N	10.80	11.00
Q	5.60	6.00
R	1.80	2.20
S	3.10	3.50
T	8.70	9.30
U	0.55	0.75

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ELECTRICAL CHARACTERISTICS

T_C=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	I_C= -3A; I_B= -0.3A			-2.0	V
$V_{BE(on)}$	Base-Emitter On Voltage	I_C= -3A; V_{CE}= -5V			-1.8	V
I_{CBO}	Collector Cutoff Current	V_{CB}= -100V; I_E= 0			-50	μA
I_{EBO}	Emitter Cutoff Current	V_{EB}= -3V; I_C= 0			-50	μA
h_{FE-1}	DC Current Gain	I_C= -20mA; V_{CE}= -5V	20			
h_{FE-2}	DC Current Gain	I_C= -1A; V_{CE}= -5V	60		200	
h_{FE-3}	DC Current Gain	I_C= -3A; V_{CE}= -5V	20			
C_{OB}	Output Capacitance	I_E= 0; V_{CE}= -10V; f_{test}=1MHz		170		pF
f_T	Current-Gain—Bandwidth Product	I_C=-0.5A; V_{CE}= -5V;f_{test}=1MHz		20		MHz

◆ **h_{FE-2} Classifications**

Q	P
60-120	100-200

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