

isc Silicon PNP Power Transistor

2SB1063

DESCRIPTION

- Low Collector Saturation Voltage : V_{CE(sat)}= -2.0V(Max)@I_C= -3A
- · Good Linearity of hFE
- Wide Area of Safe Operation
- Complement to Type 2SD1499
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

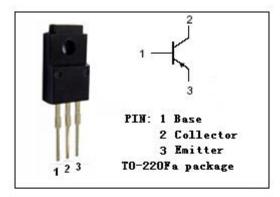


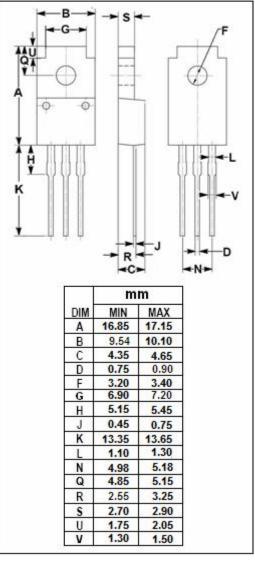
APPLICATIONS

· Designed for high power amplification.

ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER	VALUE	UNIT	
Vсво	Collector-Base Voltage	V		
Vceo	Collector-Emitter Voltage	-100	V	
V_{EBO}	Emitter-Base Voltage	-5	V	
lc	Collector Current-Continuous	А		
Ісм	Collector Current-Peak	Α		
P _C	Collector Power Dissipation @ T _a =25°C	2	W	
	Collector Power Dissipation @ T _C =25℃	40		
TJ	Junction Temperature	150	${\mathbb C}$	
T _{stg}	Storage Temperature Range -55~		${\mathbb C}$	







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ELECTRICAL CHARACTERISTICS

T_C=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{CE} (sat)	Collector-Emitter Saturation Voltage	I _C = -3A; I _B = -0.3A			-2.0	V
V _{BE(on)}	Base-Emitter On Voltage	I _C = -3A; V _{CE} = -5V			-1.8	V
I _{CBO}	Collector Cutoff Current	V _{CB} = -100V; I _E = 0			-50	μА
I _{EBO}	Emitter Cutoff Current	V _{EB} = -3V; I _C = 0			-50	μА
h _{FE-1}	DC Current Gain	I _C = -20mA; V _{CE} = -5V	20			
h _{FE-2}	DC Current Gain	I _C = -1A; V _{CE} = -5V	40		200	
h _{FE-3}	DC Current Gain	I _C = -3A; V _{CE} = -5V	20			
Сов	Output Capacitance	I _E = 0; V _{CB} = -10V; f= 1MHz		170		pF
f _T	Current-Gain—Bandwidth Product	I _C = -0.5A; V _{CE} = -5V; f _{test} =1MHz		20		MHz

♦ h_{FE-2} Classifications

R	Q	Р
40-80	60-120	100-200

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