

DATA SHEET

2SB1132

PNP GENERAL PURPOSE TRANSISTORS

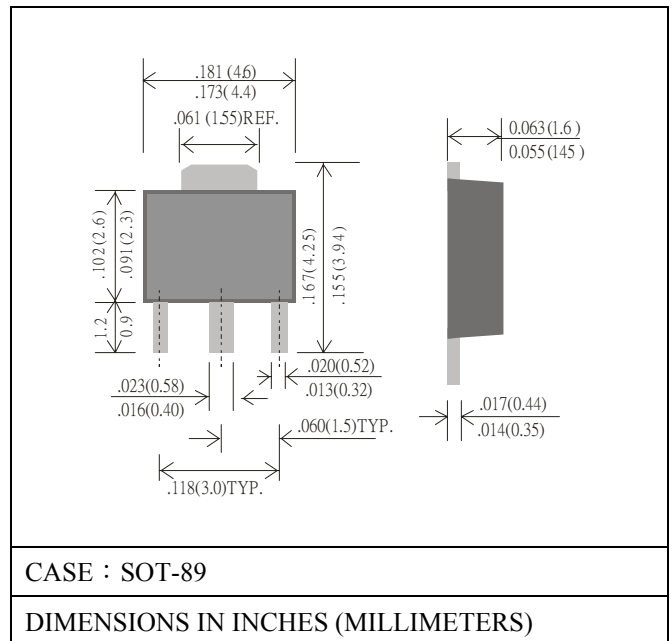
VOLTAGE -32 Volts **CURRENT** -1.0 Ampere

FEATURES

- PNP SILICON EPITAXIAL PLANAR TRANSISTOR FOR SWITCHING AND AMPLIFIER APPLICATIONS
- HIGH DC CURRENT GAIN
- LOW COLLECTOR-EMITTER SATURATION VOLTAGE

MECHANICAL DATA

- CASE : SOT-89, PLASTIC
- TERMINALS : SOLDERABLE PER MIL-STD-202, METHOD 208
- APPROX. WEIGHT: 0.002 GRAMS

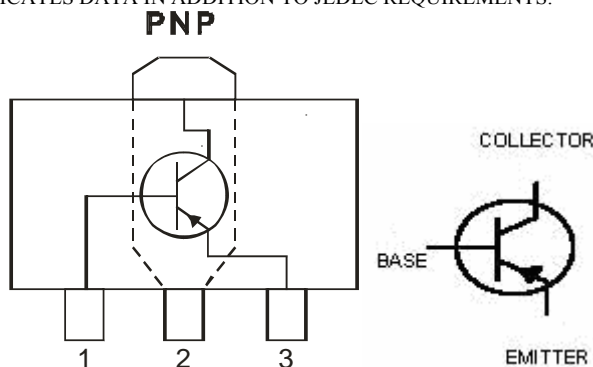


MAXIMUM RATINGS

RATINGS AT 25°C AMBIENT TEMPERATURE UNLESS OTHERWISE SPECIFIED.

PARAMETER	SYMBOL	VALUE	UNITS
COLLECTOR-EMITTER VOLTAGE	V_{CEO}	-32	V
COLLECTOR-BASE VOLTAGE	V_{CBO}	-40	V
EMITTER-BASE VOLTAGE	V_{EBO}	-5	V
COLLECTOR CURRENT-CONTINUOUS	I_C	-1.0	A
COLLECTOR POWER DISSIPATION	P_C	500	mW
JUNCTION AND STORAGE TEMPERATURE RANGE	T_j, T_{STG}	- 55 TO +150	°C

NOTE: 1. INDICATES DATA IN ADDITION TO JEDEC REQUIREMENTS.



ELECTRICAL CHARACTERISTICS
ELECTRICAL CHARACTERISTICS (A_T T_A = 25° C UNLESS OTHERWISE NOTED)
OFF CHARACTERISTICS

PARAMETER	TEST CONDITION	SYMBOL	MIN.	MAX.	UNITS
COLLECTOR-EMITTER BREAKDOWN VOLTAGE (NOTE 2)	I _C = -1mA , I _B = 0	V _{(BR)CEO}	-32	-	V
COLLECTOR-BASE BREAKDOWN VOLTAGE	I _C = -50μA , I _E = 0	V _{(BR)CBO}	-40	-	V
EMITTER-BASE BREAKDOWN VOLTAGE	I _E = -50μA , I _C = 0	V _{(BR)EBO}	-5.0	-	V
EMITTER CUT-OFF CURRENT	V _{EB} = -4V , I _C = 0	I _{EBO}	-	-0.5	μA
COLLECTOR CUT-OFF CURRENT	V _{CB} = -20V , I _E = 0	I _{CBO}	-	-0.5	μA

ON CHARACTERISTICS (NOTE 2)

DC CURRENT GAIN	I _C = -100mA , V _{CE} = -3V	h _{FE}	82	390	
COLLECTOR-EMITTER SATURATION VOLTAGE	I _C = -500mA , I _B = -50mA	V _{CE(SAT)}	-	-0.5	V

SMALL-SIGNAL CHARACTERISTICS

INPUT CAPACITANCE	V _{CB} = -10V , I _E = 0, f = 1.0MHz	C _{ob}	20 (TYP.) 30 (MAX.)		pF
CURRENT-GAIN-BANDWIDTH PRODUCT	I _C = -50mA , V _{CE} = -5V, f = 30MHz	f _T	150 (TYP.)		MHz

NOTE: 2. PULSE TEST: PULSE WIDTH ≤ 300μs; DUTY CYCLE ≤ 2%.

CLASSIFICATION OF h_{FE}

RANK	P	Q	R
RANGE	82~180	120~270	180~390

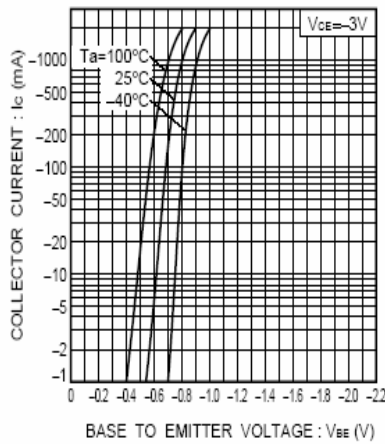


Fig.1 Grounded emitter propagation characteristics

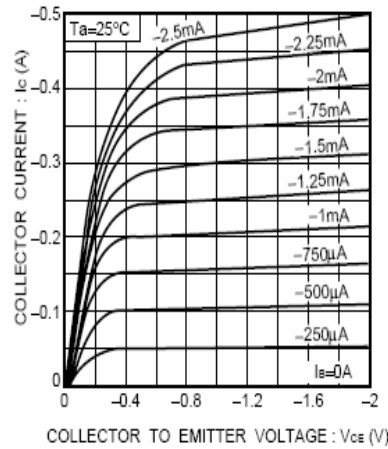


Fig.2 Grounded emitter output characteristics

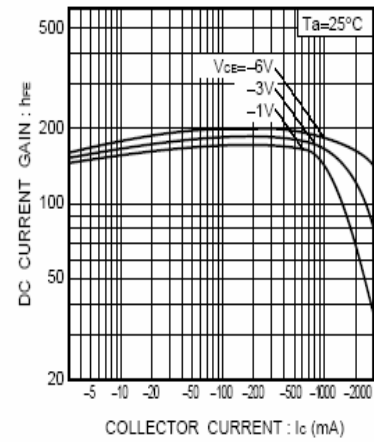


Fig.3 DC current gain vs. collector current (I)

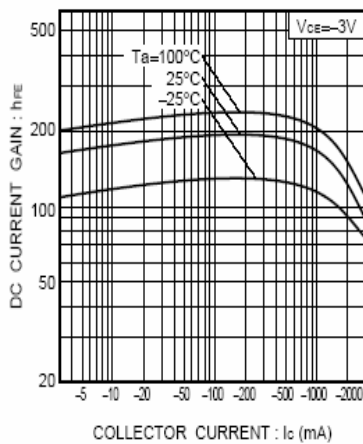


Fig.4 DC current gain vs. collector current (II)

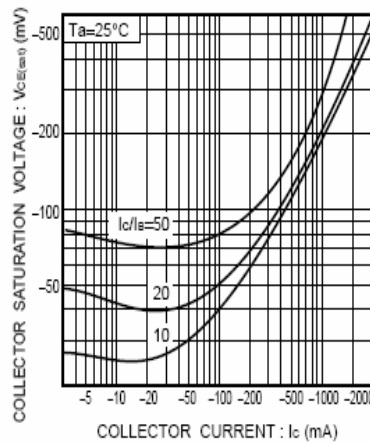


Fig.5 Collector-emitter saturation voltage vs. collector current (I)

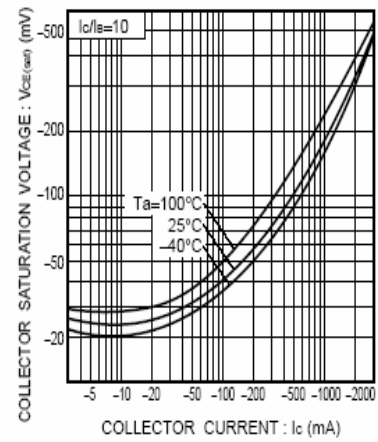


Fig.6 Collector-emitter saturation voltage vs. collector current (II)

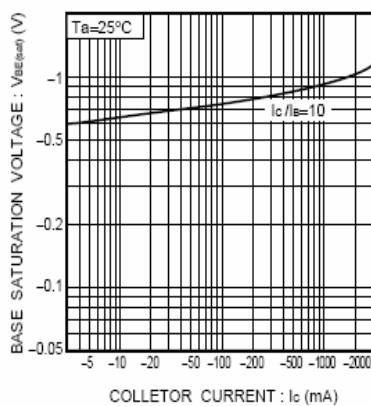


Fig.7 Base-emitter saturation voltage vs. collector current

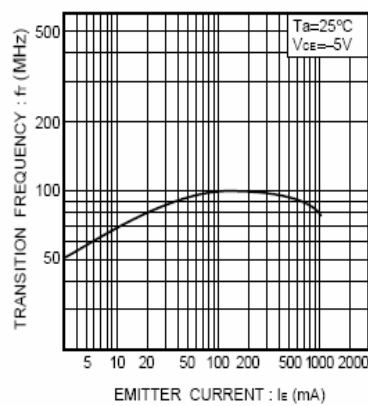


Fig.8 Gain bandwidth product vs. emitter current

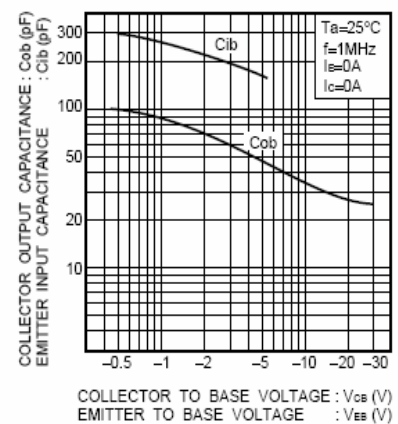


Fig.9 Collector output capacitance vs. collector-base voltage
Emitter input capacitance vs. emitter-base voltage

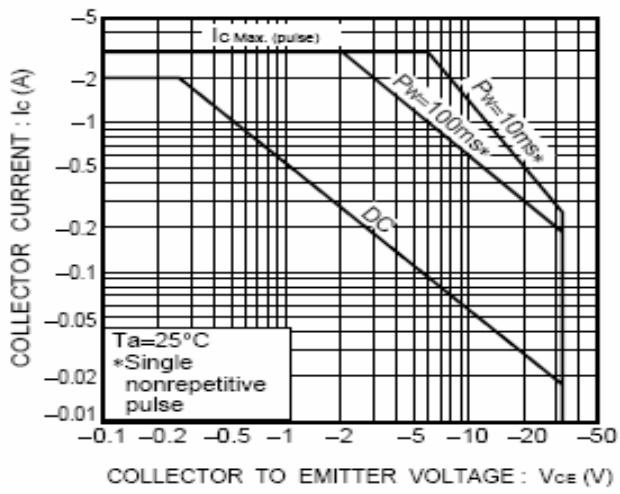


Fig.10 Safe operation area