

## **isc** Silicon PNP Power Transistor

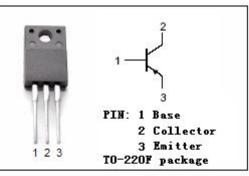
# 2SB1134

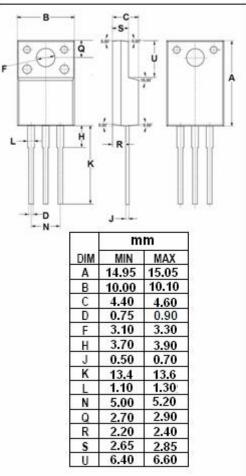
## DESCRIPTION

- Collector-Emitter Breakdown Voltage-
  - : V<sub>(BR)CEO</sub>= -50V(Min)
- Low Collector Saturation Voltage-
- : V<sub>CE(sat)</sub>= -0.4V(Max.)@ I<sub>C</sub>= -3A
- Complement to Type 2SD1667
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

### **APPLICATIONS**

• Designed for relay drivers, high-speed inverters, and other general high-current switching applications.





## ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER	VALUE	UNIT	
V <sub>CBO</sub>	Collector-Base Voltage	-60	V	
Vceo	Collector-Emitter Voltage -50			
V <sub>EBO</sub>	Emitter-Base Voltage	-6	V	
lc	Collector Current-Continuous -5		A	
Ісм	Collector Current-Peak	-9	А	
	Collector Power Dissipation @Ta=25℃	2	W	
Pc	Collector Power Dissipation @T <sub>c</sub> =25°C	25		
TJ	Junction Temperature	150	°C	
T <sub>stg</sub>	Storage Temperature	-55~150	°C	

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1



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## **ELECTRICAL CHARACTERISTICS**

#### Tj=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	МАХ	UNIT
V <sub>(BR)CEO</sub>	Collector-Emitter Breakdown Voltage	I <sub>C</sub> = -1mA; R <sub>BE</sub> = ∞	-50			V
V <sub>(BR)CBO</sub>	Collector-Base Breakdown Voltage	I <sub>C</sub> = -1mA; I <sub>E</sub> = 0	-60			V
V <sub>(BR)EBO</sub>	Emitter-Base Breakdown Voltage	I <sub>E</sub> = -1mA; I <sub>C</sub> = 0	-6			V
V <sub>CE</sub> (sat)	Collector-Emitter Saturation Voltage	I <sub>C</sub> = -3A; I <sub>B</sub> = -0.3A			-0.4	V
I <sub>CBO</sub>	Collector Cutoff Current	V <sub>CB</sub> = -40V; I <sub>E</sub> =0			-100	μA
I <sub>EBO</sub>	Emitter Cutoff Current	V <sub>EB</sub> = -4V; I <sub>C</sub> =0			-100	μA
h <sub>FE-1</sub>	DC Current Gain	I <sub>C</sub> = -1A; V <sub>CE</sub> = -2V	70		280	
h <sub>FE-2</sub>	DC Current Gain	I <sub>C</sub> = -3A; V <sub>CE</sub> = -2V	30			

#### h<sub>FE-1</sub> Classifications

Q	R	S	
70-140	100-200	140-280	

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