



20V/5A Switching Applications

Applications

· Strobes, power supplies, relay drivers, lamp drivers.

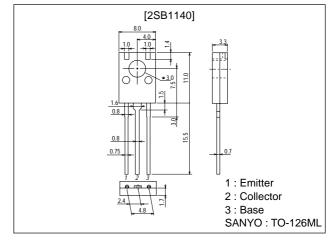
Features

- · Adoption of FBET, MBIT processes.
- · Low saturation voltage.
- · Large current capacity.
- · Short switching time.

Package Dimensions

unit:mm

2042B



Specifications

Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	V _{CBO}		-25	V
Collector-to-Emitter Voltage	VCEO		-20	V
Emitter-to-Base Voltage	V _{EBO}		-5	V
Collector Current	I _C		-5	Α
Collector Current (Pulse)	I _{CP}		-8	Α
Base Current	I _B		-0.5	Α
Collector Dissipation	Ъ		1.5	W
	PC	Tc=25°C	10	W
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta = 25°C

Parameter	Symbol	Conditions	Ratings			Unit
r arameter			min	typ	max	Offic
Collector Cutoff Current	I _{CBO}	V _{CB} =-20V, I _E =0			-500	nA
Emitter Cutoff Current	IEBO	V _{EB} =-4V, I _C =0			-500	nA
DC Current Gain	h _{FE} 1	V _{CE} =-2V, I _C =-500mA	100*		400*	
DO Guiterit Gairi	h _{FE} 2	V _{CE} =-2V, I _C =-4A	60	·		
Gain-Bandwidth Product	fT	V _{CE} =-5V, I _C =-200mA		320		MHz

^{*}: The 2SB1140 is classified by 500mA h_{FE} as follows:

Rank	R	S	Т		
h _{FE}	100 to 200	140 to 280	200 to 400		

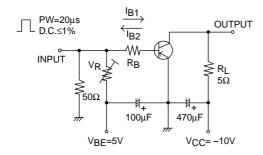
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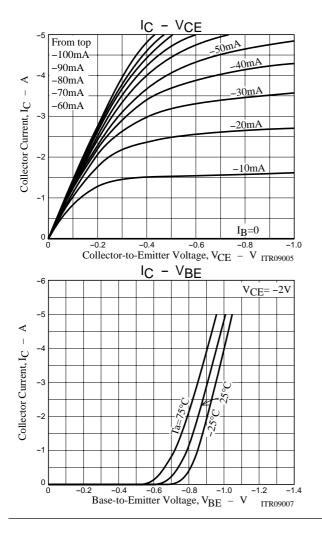
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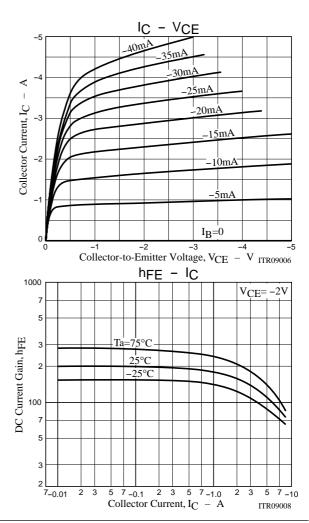
Parameter	Symbol	Conditions	Ratings			Unit
Farameter	Symbol		min	typ	max	Offic
Output Capacitance	C _{ob}	V _{CB} =-10V, f=1MHz		60		pF
Collector-to-Emitter Saturation Voltage	V _{CE(sat)}	I _C =-3A, I _B =-60mA		-250	-500	mV
Base-to-Emitter Saturation Voltage	V _{BE(sat)}	I _C =-3A, I _B =-60mA		-1.0	-1.3	V
Collector-to-Base Breakdown Voltage	V(BR)CBO	I _C =(-)10μA, I _E =0	-25			V
Collector-to-Emitter Breakdown Voltage	V(BR)CEO	I _C =(−)1mA, R _{BE} =∞	-20			V
Emitter-to-Base Breakdown Voltage	V(BR)EBO	I _E =(-)10μA, I _C =0	-5			V
Turn-ON Time	ton	See specified Test Circuti.		40		ns
Storage Time	t _{stg}	See specified Test Circuit.		200		ns
Fall Time	t _f	See specified Test Circuit.		10		ns

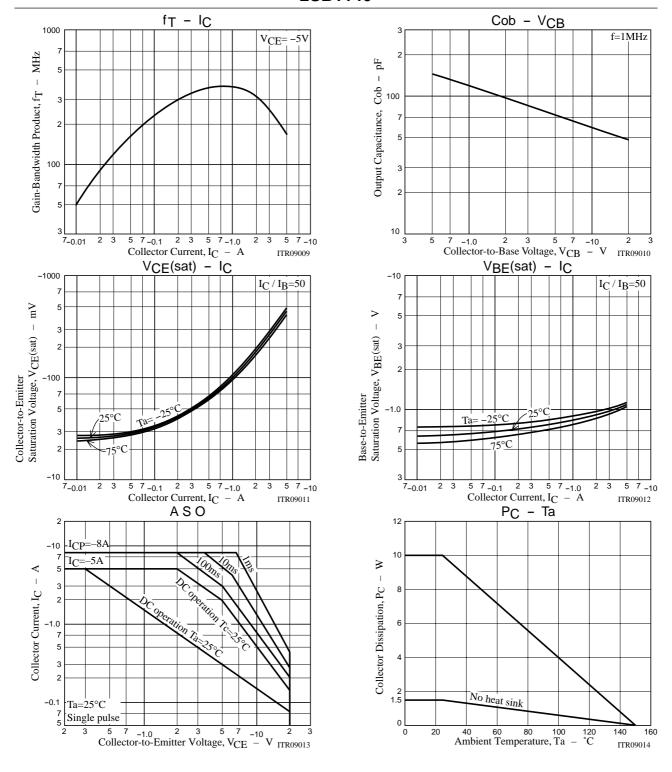
Switching Time Test Circuit



 $I_{C}=10I_{B1}=-10I_{B2}=-2A$ (For PNP, the polarity is reversed.)







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