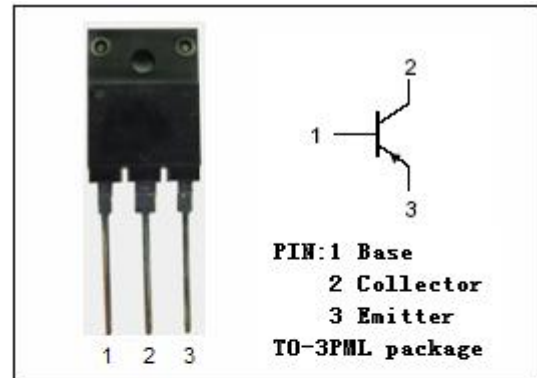


**isc Silicon PNP Power Transistor**
**2SB1160**
**DESCRIPTION**

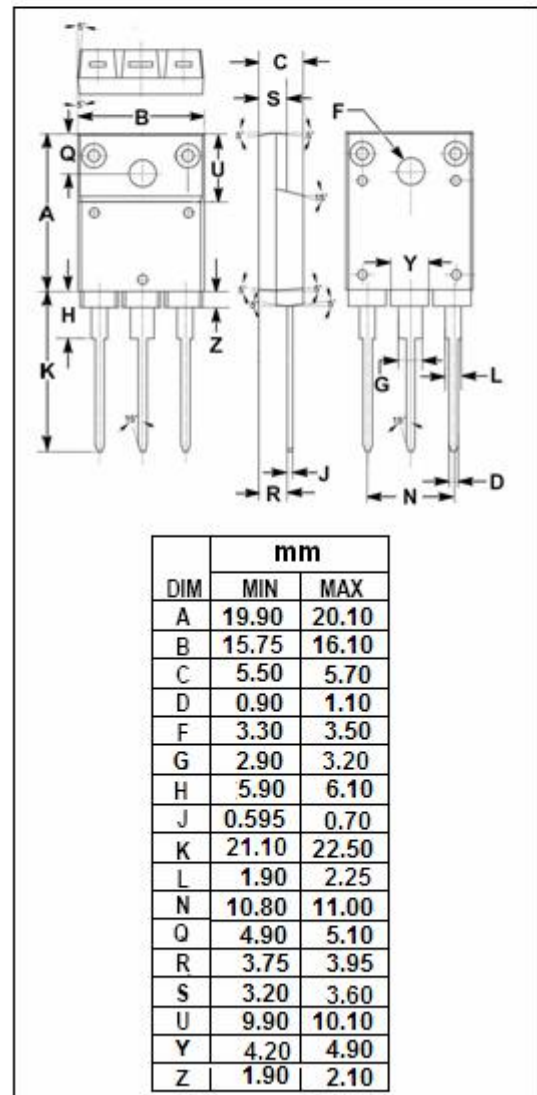
- Collector-Emitter Breakdown Voltage-  
:  $V_{(BR)CEO} = -150V(\text{Min})$
- Good Linearity of  $h_{FE}$
- Wide Area of Safe Operation
- Complement to Type 2SD1715
- Minimum Lot-to-Lot variations for robust device performance and reliable operation


**APPLICATIONS**

- Designed for high power amplifier applications

**ABSOLUTE MAXIMUM RATINGS( $T_a=25^\circ\text{C}$ )**

SYMBOL	PARAMETER	VALUE	UNIT
$V_{CBO}$	Collector-Base Voltage	-150	V
$V_{CEO}$	Collector-Emitter Voltage	-150	V
$V_{EBO}$	Emitter-Base Voltage	-5	V
$I_C$	Collector Current-Continuous	-9	A
$I_{CP}$	Collector Current-Pulse	-15	A
$P_C$	Collector Power Dissipation @ $T_C=25^\circ\text{C}$	100	W
	Collector Power Dissipation @ $T_a=25^\circ\text{C}$	3	
$T_J$	Junction Temperature	150	$^\circ\text{C}$
$T_{stg}$	Storage Temperature Range	-55~150	$^\circ\text{C}$



**isc Silicon PNP Power Transistor**
**2SB1160**
**ELECTRICAL CHARACTERISTICS**

 T<sub>C</sub>=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V <sub>CE(sat)</sub>	Collector-Emitter Saturation Voltage	I <sub>C</sub> = -7A; I <sub>B</sub> = -0.7A			-2.0	V
V <sub>BE(on)</sub>	Base -Emitter On Voltage	I <sub>C</sub> = -7A; V <sub>CE</sub> = -5V			-1.8	V
I <sub>CBO</sub>	Collector Cutoff Current	V <sub>CB</sub> = -150V; I <sub>E</sub> = 0			-50	μ A
I <sub>EBO</sub>	Emitter Cutoff Current	V <sub>EB</sub> = -3V; I <sub>C</sub> = 0			-50	μ A
h <sub>FE-1</sub>	DC Current Gain	I <sub>C</sub> = -20mA; V <sub>CE</sub> = -5V	20			
h <sub>FE-2</sub>	DC Current Gain	I <sub>C</sub> = -1A; V <sub>CE</sub> = -5V	60		200	
h <sub>FE-3</sub>	DC Current Gain	I <sub>C</sub> = -7A; V <sub>CE</sub> = -5V	20			

**◆ h<sub>FE-2</sub> Classifications**

Q	S	P
60-120	80-160	100-200

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