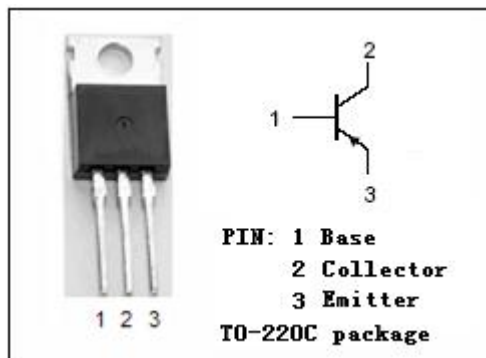


**isc Silicon PNP Power Transistor**
**2SB1334**
**DESCRIPTION**

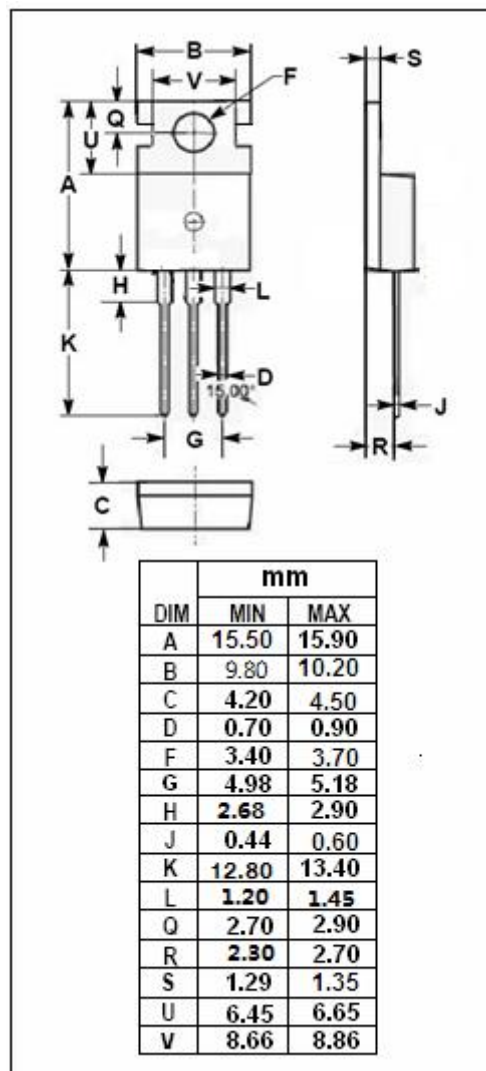
- High Collector Current:  $I_C = -4A$
- Low Collector Saturation Voltage  
:  $V_{CE(sat)} = -1.5V(\text{Max}) @ I_C = -3A$
- Wide Area of Safe Operation
- Complement to Type 2SD1778
- Minimum Lot-to-Lot variations for robust device performance and reliable operation


**APPLICATIONS**

- Designed for low frequency power amplifier applications.

**ABSOLUTE MAXIMUM RATINGS ( $T_a = 25^\circ\text{C}$ )**

SYMBOL	PARAMETER	VALUE	UNIT
$V_{CBO}$	Collector-Base Voltage	-80	V
$V_{CEO}$	Collector-Emitter Voltage	-60	V
$V_{EBO}$	Emitter-Base Voltage	-5	V
$I_C$	Collector Current-Continuous	-4	A
$I_{CM}$	Collector Current-Peak	-6	A
$P_C$	Total Power Dissipation @ $T_C = 25^\circ\text{C}$	40	W
$T_J$	Junction Temperature	150	$^\circ\text{C}$
$T_{stg}$	Storage Temperature Range	-55~150	$^\circ\text{C}$



**isc Silicon PNP Power Transistor**
**2SB1334**
**ELECTRICAL CHARACTERISTICS**

 T<sub>c</sub>=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V <sub>(BR)CEO</sub>	Collector-Emitter Breakdown Voltage	I <sub>C</sub> = -1mA; I <sub>B</sub> = 0	-60			V
V <sub>(BR)CBO</sub>	Collector-Base Breakdown Voltage	I <sub>C</sub> = -50 μ A; I <sub>E</sub> = 0	-80			V
V <sub>(BR)EBO</sub>	Emitter-Base Breakdown Voltage	I <sub>E</sub> = -50 μ A; I <sub>C</sub> = 0	-5			V
V <sub>CE(sat)</sub>	Collector-Emitter Saturation Voltage	I <sub>C</sub> = -3A; I <sub>B</sub> = -0.3A			-1.5	V
V <sub>BE(sat)</sub>	Base-Emitter Saturation Voltage	I <sub>C</sub> = -3A; I <sub>B</sub> = -0.3A			-1.5	V
I <sub>CBO</sub>	Collector Cutoff Current	V <sub>CB</sub> = -80V; I <sub>E</sub> = 0			-10	μ A
I <sub>EBO</sub>	Emitter Cutoff Current	V <sub>EB</sub> = -4V; I <sub>C</sub> = 0			-10	μ A
h <sub>FE</sub>	DC Current Gain	I <sub>C</sub> = -1A; V <sub>CE</sub> = -5V	60		320	

**◆ h<sub>FE</sub> Classifications**

D	E	F
60-120	100-200	160-320

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