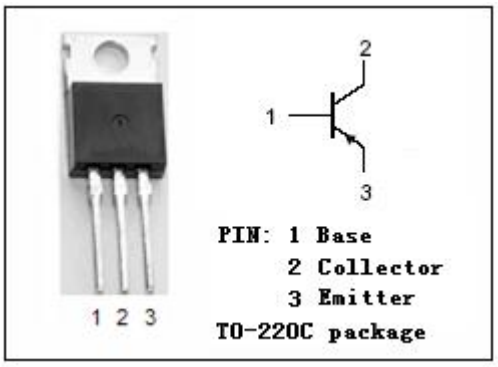
isc Silicon PNP Power Transistor — 2SB1334

DESCRIPTION

- High Collector Current:: I_C= -4A
- Low Collector Saturation Voltage
  : $V_{CE(sat)}$= -1.5V(Max)@I_C= -3A
- Wide Area of Safe Operation
- Complement to Type 2SD1778
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

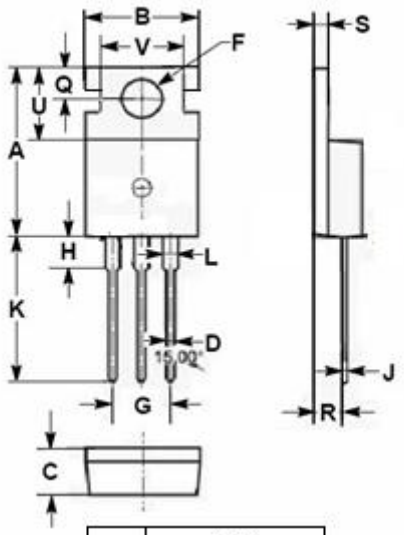


APPLICATIONS

- Designed for low frequency power amplifier applications.

ABSOLUTE MAXIMUM RATINGS(T_a=25℃)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	-80	V
V_{CEO}	Collector-Emitter Voltage	-60	V
V_{EBO}	Emitter-Base Voltage	-5	V
I_C	Collector Current-Continuous	-4	A
I_{CM}	Collector Current-Peak	-6	A
P_C	Total Power Dissipation @ T_C=25℃	40	W
T_J	Junction Temperature	150	℃
T_{stg}	Storage Temperature Range	-55~150	℃



DIM	mm MIN	MAX
A	15.50	15.90
B	9.80	10.20
C	4.20	4.50
D	0.70	0.90
F	3.40	3.70
G	4.98	5.18
H	2.68	2.90
J	0.44	0.60
K	12.80	13.40
L	1.20	1.45
Q	2.70	2.90
R	2.30	2.70
S	1.29	1.35
U	6.45	6.65
V	8.66	8.86

isc Silicon PNP Power Transistor 2SB1334

ELECTRICAL CHARACTERISTICS

T_C=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
$V_{(BR)CEO}$	Collector-Emitter Breakdown Voltage	I_C= -1mA; I_B= 0	-60			V
$V_{(BR)CBO}$	Collector-Base Breakdown Voltage	I_C= -50 μA; I_E= 0	-80			V
$V_{(BR)EBO}$	Emitter-Base Breakdown Voltage	I_E= -50 μA; I_C= 0	-5			V
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	I_C= -3A; I_B= -0.3A			-1.5	V
$V_{BE(sat)}$	Base-Emitter Saturation Voltage	I_C= -3A; I_B= -0.3A			-1.5	V
I_{CBO}	Collector Cutoff Current	V_{CB}= -80V; I_E= 0			-10	μA
I_{EBO}	Emitter Cutoff Current	V_{EB}= -4V; I_C= 0			-10	μA
h_{FE}	DC Current Gain	I_C= -1A; V_{CE}= -5V	60		320	

◆ **h_{FE} Classifications**

D	E	F
60-120	100-200	160-320