

Silicon PNP Power Transistors

2SB1392

DESCRIPTION

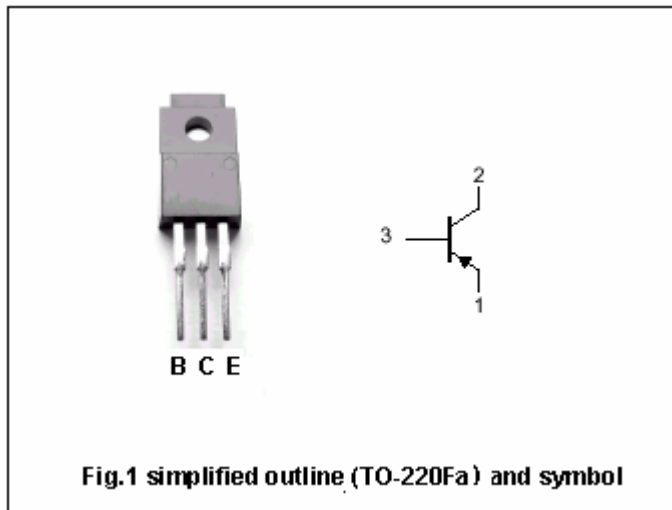
- With TO-220Fa package
- Low collector saturation voltage
- Wide area of safe operation

APPLICATIONS

- For low frequency power amplifier applications

PINNING

PIN	DESCRIPTION
1	Emitter
2	Collector
3	Base



Absolute maximum ratings(Ta=25°C)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V <sub>CB0</sub>	Collector-base voltage	Open emitter	-70	V
V <sub>CEO</sub>	Collector -emitter voltage	Open base	-60	V
V <sub>EBO</sub>	Emitter-base voltage	Open collector	-5	V
I <sub>C</sub>	Collector current		-4	A
I <sub>CM</sub>	Collector current-peak		-8	A
P <sub>C</sub>	Collector power dissipation	T <sub>a</sub> =25°C	2.0	W
		T <sub>C</sub> =25°C	25	
T <sub>j</sub>	Junction temperature		150	°C
T <sub>stg</sub>	Storage temperature		-55~150	°C

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## CHARACTERISTICS

T<sub>j</sub>=25 °C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V <sub>(BR)CEO</sub>	Collector-emitter breakdown voltage	I <sub>C</sub> =-50mA; R <sub>BE</sub> =∞	-60			V
V <sub>(BR)CBO</sub>	Collector-base breakdown voltage	I <sub>C</sub> =-10μA; I <sub>E</sub> =0	-70			V
V <sub>(BR)EBO</sub>	Emitter-base breakdown voltage	I <sub>E</sub> =-10μA; I <sub>C</sub> =0	-5			V
V <sub>CEsat</sub>	Collector-emitter saturation voltage	I <sub>C</sub> =-2A; I <sub>B</sub> =-0.2A			-1.0	V
V <sub>BEsat</sub>	Base-emitter saturation voltage	I <sub>C</sub> =-2A; I <sub>B</sub> =-0.2A			-1.2	V
V <sub>BE</sub>	Base-emitter voltage	I <sub>C</sub> =-1A; V <sub>CE</sub> =-4V			-1.0	V
I <sub>CBO</sub>	Collector cut-off current	V <sub>CB</sub> =-50V; I <sub>E</sub> =0			-10	μA
I <sub>CEO</sub>	Collector cut-off current	V <sub>CE</sub> =-50V; R <sub>BE</sub> =∞			-10	μA
h <sub>FE-1</sub>	DC current gain	I <sub>C</sub> =-1A; V <sub>CE</sub> =-4V	60		200	
h <sub>FE-2</sub>	DC current gain	I <sub>C</sub> =-0.1A; V <sub>CE</sub> =-4V	35			

◆ h<sub>FE-1</sub> classifications

B	C
60-120	100-200

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PACKAGE OUTLINE

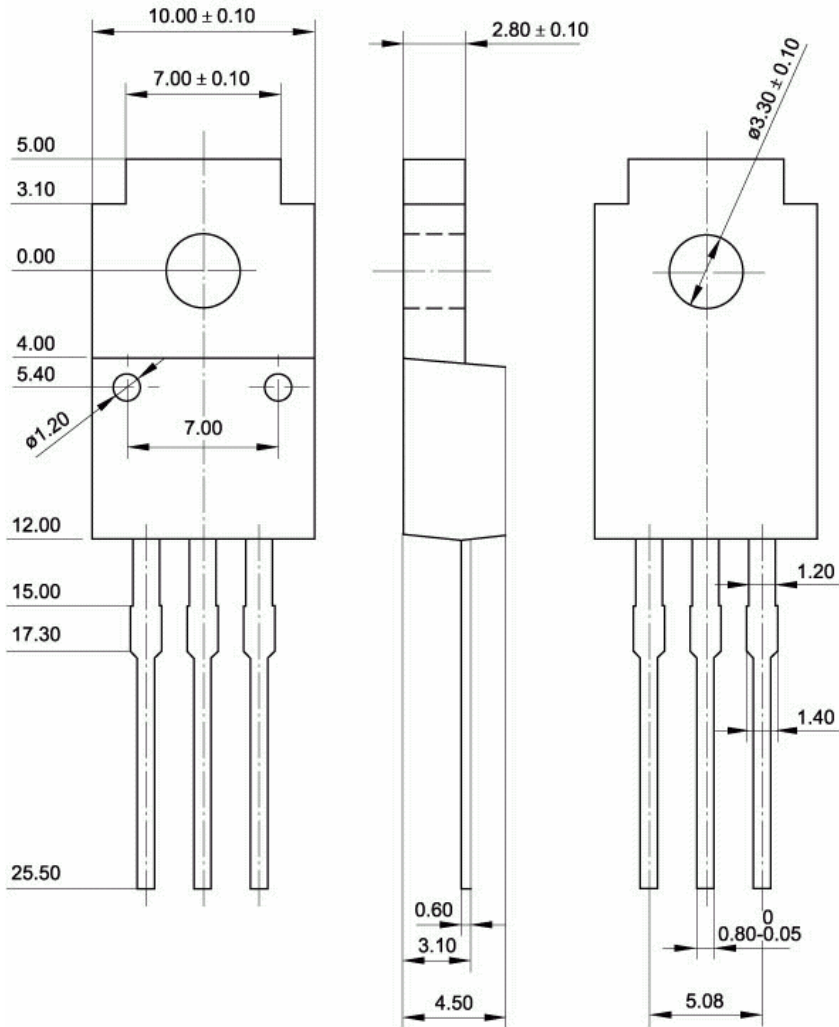


Fig.2 Outline dimensions (unindicated tolerance:  $\pm 0.15$  mm)