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isc Silicon PNP Darlington Power Transistor

2SB1404

 DESCRIPTION Collector-Emitter Breakdown Voltage- : V_{(BR)CEO}= -120V(Min) High DC Current Gain- : h_{FE}= 1000(Min)@ (V_{CE}= -3V, I_C= -1.5A) Minimum Lot-to-Lot variations for robust device performance and reliable operation 					PIN: 1 Base 2 Collector 3 Emitter T0-220Fa package					
-	TIONS ed for low frequency power amp FE MAXIMUM RATINGS(Ta=25°		ations.			→ S		6		
SYMBOL	PARAMETER	VALUE	UNIT	ĸ ļ				•		
Vсво	Collector-Base Voltage	-120	V	J	μų	→ R ∢C	≼_J ≼	 - N-		
V _{CEO}	Collector-Emitter Voltage	-120	V		DIM	mm MIN MAX]		
V _{EBO}	Emitter-Base Voltage	-7	V		A B C	16.85 9.54 4.35	17.15 10.10 4.65			
lc	Collector Current-Continuous	-3	A		D F G	0.75 3.20 6.90	0.90 3.40 7.20			
Ісм	Collector Current-Peak	-6	A		H J K	5.15 0.45 13.35	5.45 0.75 13.65			
	Collector Power Dissipation @T _a =25℃	2	W		N Q	1.10 4.98 4.85	1.30 5.18 5.15			
Pc	Collector Power Dissipation @T _c =25℃	25			R S U V	2.55 2.70 1.75 1.30	3.25 2.90 2.05	-		
TJ	Junction Temperature	150	°C		v	1.39	1.50	1		
T _{stg}	Storage Temperature	-55~150	°C							

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ELECTRICAL CHARACTERISTICS

Tj=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	МАХ	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = -25mA; R _{BE} = ∞	-120			V
V _{(BR)CBO}	Collector-Base Breakdown Voltage	l _c = -0.1mA; l _E = 0	-120			V
V _{(BR)EBO}	Emitter-Base Breakdown Voltage	I _E = -50mA; I _C = 0	-7			V
V _{CE(sat)} -1	Collector-Emitter Saturation Voltage	I _C = -1.5A; I _B = -3mA			-1.5	V
V _{CE(sat)} -2	Collector-Emitter Saturation Voltage	I _C = -3A; I _B = -30mA			-3.0	V
V _{BE(sat)} -1	Base-Emitter Saturation Voltage	I _C = -1.5A; I _B = -3mA			-2.0	V
V _{BE(sat)-2}	Base-Emitter Saturation Voltage	Ic= -3A; I _B = -30mA			-3.5	V
I _{CBO}	Collector Cutoff Current	V _{CB} = -100V; I _E = 0			-10	μA
I _{CEO}	Collector Cutoff Current	V _{CE} = -100V; R _{BE} = ∞			-10	μA
h _{FE}	DC Current Gain	I _C = -1.5A; V _{CE} = -3V	1000		20000	

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