

isc Silicon PNP Power Transistor

2SB1421

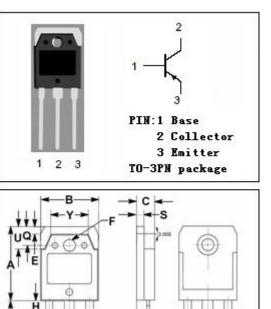
DESCRIPTION

- · Collector-Emitter Breakdown Voltage-
- : V_{(BR)CEO}= -140V(Min)
- Wide Area of Safe Operation
- Complement to Type 2SD2140
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- · Designed for high power amplifications.
- · Optimum for the output stage of a HiFi audio amplifier

ABSOLUTE MAXIMUM RATINGS(Ta=25℃)				
SYMBOL	PARAMETER	VALUE	UNIT	
V _{сво}	Collector-Base Voltage -140		V	
V _{CEO}	Collector-Emitter Voltage	-140	V	
V _{EBO}	Emitter-Base Voltage	-5	V	
lc	Collector Current-Continuous	-7	A	
I _{CP}	Collector Current-Pulse	-12	A	
Pc	Collector Power Dissipation @ T _C =25℃	80	w	
	Collector Power Dissipation @ T₂=25℃	2.5		
TJ	Junction Temperature	150	°C	
T _{stg}	Storage Temperature Range	-55~150	°C	



	82		N
	m	m	3
DIM	MIN	MAX	0
Α	19.60	20.10	
В	15.50	15.70	0
С	4.70	4.90	1
D	0.90	1.10	8
E	1.90	2.10	
F	3.40	3.60	Ú.
G	2.90	3.20	
Η	3.20	3.40	
J	0.595	0.605	8
Κ	20.00	20.70	3
L	1.90	2.20	0
Ν	10.89	10.91	Ŭ.
Q	4.90	5.10	
R	3.35	3.45	8
S	1.995	2.100	3
Ŭ	5.90		0
Y	9.90	6.10 10.10	

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ELECTRICAL CHARACTERISTICS

$T_c=25^{\circ}C$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	МАХ	UNIT
V _{CE} (sat)	Collector-Emitter Saturation Voltage	I _C = -5A; I _B = -0.5A			-2.0	v
VBE(on)	Base -Emitter On Voltage	I _C = -5A; V _{CE} = -5V			-1.8	V
I _{CBO}	Collector Cutoff Current	V _{CB} = -140V; I _E = 0			-50	μA
I _{EBO}	Emitter Cutoff Current	V _{EB} = -3V; I _C = 0			-50	μA
h _{FE-1}	DC Current Gain	I _C = -20mA; V _{CE} = -5V	20			
h _{FE-2}	DC Current Gain	I _C = -1A; V _{CE} = -5V	60		200	
h _{FE-3}	DC Current Gain	I _C = -5A; V _{CE} = -5V	20			
f⊤	Current-Gain—Bandwidth Product	I _C = -0.5A; V _{CE} = -5 V; f= 1MHz		15		MHz

h_{FE-2}Classifications

Q	S	Р
60-120	80-160	100-200

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