

INCHANGE SEMICONDUCTOR

isc Silicon PNP Darlington Power Transistor

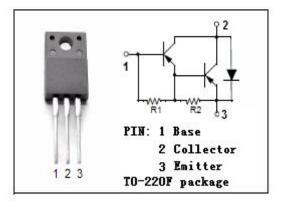
2SB1430

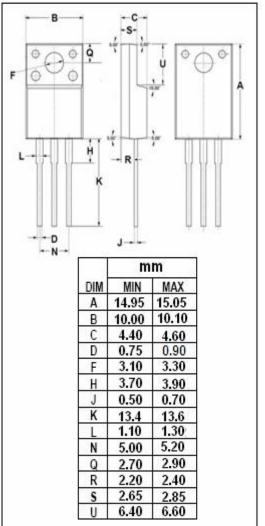
DESCRIPTION

- Collector-Emitter Breakdown Voltage : V_{(BR)CEO}= -100V(Min)
- High DC Current Gain-
- : h_{FE}= 2000(Min)@ (V_{CE}= -2V, I_C= -2A)
- Low Collector Saturation Voltage-
 - : $V_{CE(sat)}$ = -1.5V(Max)@ (I_C= -2A, I_B= -2mA)
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

• Designed for low-frequency power amplifiers and lowspeed switching applications.





ABSOLUTE MAXIMUM RATINGS(Ta=25°C)

SYMBOL	PARAMETER	VALUE	UNIT
V _{CBO}	Collector-Base Voltage	-100	V
Vceo	Collector-Emitter Voltage	-100	V
V_{EBO}	Emitter-Base Voltage	-7	V
lc	Collector Current-Continuous	-5	A
I _{CM}	Collector Current-Peak	-10	А
I _B	Base Current-Continuous	-0.5	А
Pc	Collector Power Dissipation @T _a =25°C	2	14/
	Collector Power Dissipation @Tc=25°C	20	W
TJ	Junction Temperature	150	°C
T _{stg}	Storage Temperature	-55~150	°C

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ELECTRICAL CHARACTERISTICS

Tj=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	МАХ	UNIT
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = -2A; I _B = -2mA			-1.5	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = -2Α; I _B = -2mΑ			-2.0	V
І _{сво}	Collector Cutoff Current	V _{CB} = -100V; I _E = 0			-1.0	μA
h _{FE-1}	DC Current Gain	I _C = -2A; V _{CE} = -2V	2000		20000	
h _{FE-2}	DC Current Gain	I _C = -4A; V _{CE} = -2V	500			
Сов	Output Capacitance	I _E = 0; V _{CB} = -10V; f _{test} = 1MHz		60		pF

Switching Times

t _{on}	Turn-on Time		0.5	μ S
t _{stg}	Storage Time	I_{C} = -2A, I_{B1} = - I_{B2} = -2mA, V _{CC} ≈ -50V; R _L = 25 Ω	1.0	μ S
t _f	Fall Time		1.0	μ S

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• h_{FE-1} Classifications

М	L	K
2000-5000	4000-10000	8000-20000



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