

isc Silicon PNP Power Transistor
2SB1454
DESCRIPTION

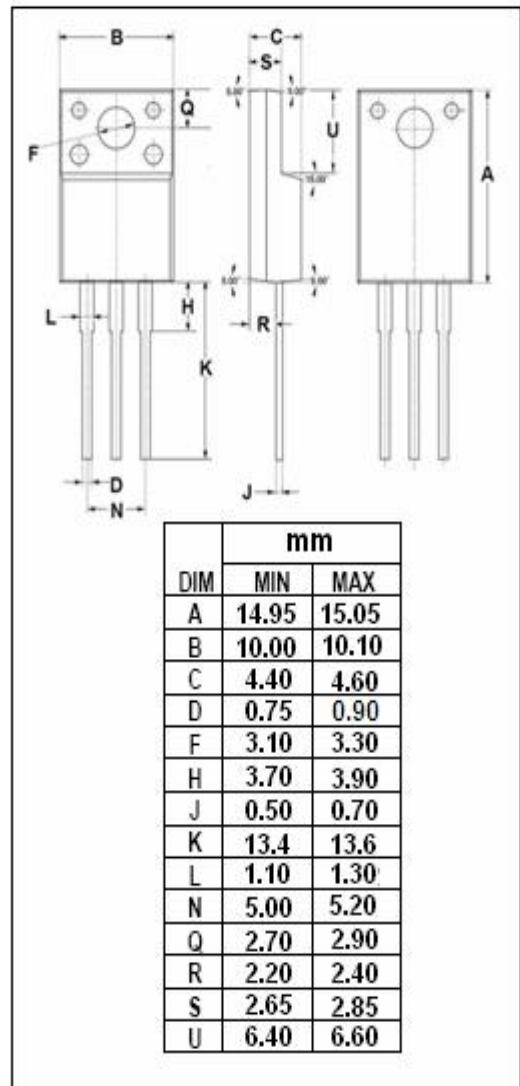
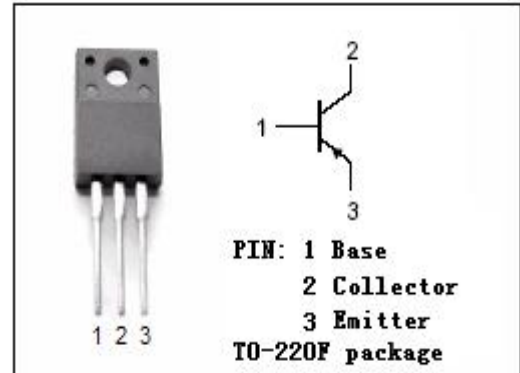
- Collector-Emitter Breakdown Voltage-
: $V_{(BR)CEO} = -80V(\text{Min})$
- Low Collector Saturation Voltage-
: $V_{CE(\text{sat})} = -0.5V(\text{Max}) @ (I_C = -3A, I_B = -0.3A)$
- Complement to Type 2SD2202
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Designed for high-current switching applications.

ABSOLUTE MAXIMUM RATINGS($T_a=25^\circ\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	-90	V
V_{CEO}	Collector-Emitter Voltage	-80	V
V_{EBO}	Emitter-Base Voltage	-6	V
I_C	Collector Current-Continuous	-5	A
I_{CM}	Collector Current-Pulse	-9	A
P_C	Collector Power Dissipation @ $T_a=25^\circ\text{C}$	2	W
	Collector Power Dissipation @ $T_c=25^\circ\text{C}$	25	
T_J	Junction Temperature	150	$^\circ\text{C}$
T_{stg}	Storage Temperature	-55~150	$^\circ\text{C}$



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ELECTRICAL CHARACTERISTICS

 T_j=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = -1mA; R _{BE} = ∞	-80			V
V _{(BR)CBO}	Collector-Base Breakdown Voltage	I _C = -1mA; I _E = 0	-90			V
V _{(BR)EBO}	Emitter-Base Breakdown Voltage	I _E = -1mA; I _C = 0	-6			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = -3A; I _B = -0.3A			-0.5	V
I _{CBO}	Collector Cutoff Current	V _{CB} = -80V; I _E = 0			-100	μ A
I _{EBO}	Emitter Cutoff Current	V _{EB} = -4V; I _C = 0			-100	μ A
h _{FE-1}	DC Current Gain	I _C = -1A; V _{CE} = -2V	70		280	
h _{FE-2}	DC Current Gain	I _C = -3A; V _{CE} = -2V	30			
f _T	Current-Gain—Bandwidth Product	I _C = -1A; V _{CE} = -5V		20		MHz

Switching Times

t _{on}	Turn-on Time	V _{CC} = -50V, R _L = 25 Ω, I _C = -2A; I _{B1} = -I _{B2} = -0.2A,		0.2		μ s
t _{stg}	Storage Time			0.7		μ s
t _f	Fall Time			0.2		μ s

◆ h_{FE-1} Classifications

Q	R	S
70-140	100-200	140-280

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