

isc Silicon PNP Power Transistor

2SB530

DESCRIPTION

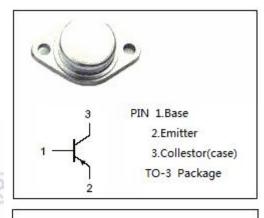
- Collector-Emitter Sustaining Voltage-: V_{CEO(SUS)}= -110V(Min)
- Low Collector Saturation Voltage-: V_{CE(sat)}= -1.0V(Max.) @I_C= -5A
- Wide area of safe operation
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

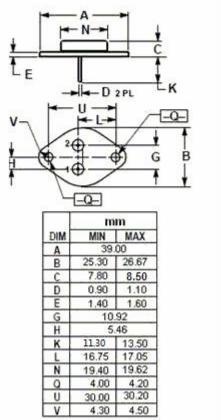
APPLICATIONS

• Designed for general-purpose switching and amplifier applications

SYMBOL	PARAMETER	VALUE	UNIT	
V _{CBO}	Collector-Base Voltage	-150	V	
V _{CEO}	Collector-Emitter Voltage	-110	v	
V_{EBO}	Emitter-Base Voltage	-5	V	
Ιc	Collector Current-Continuous	-8	A	
Pc	Collector Power Dissipation @Tc=25°C	80	W	
TJ	Junction Temperature	150	°C	
T _{stg}	Storage Temperature	-55~150	°C	

ABSOLUTE MAXIMUM RATINGS(Ta=25℃)







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ELECTRICAL CHARACTERISTICS

Tj=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	МАХ	UNIT
V _{CEO(SUS)}	Collector-Emitter Sustaining Voltage	I _C = -10mA; I _B = 0	-110			v
V _{(BR)EBO}	Emitter-Base Breakdown Voltage	I _E = -1mA; I _C = 0	-8			v
V _{(BR)CBO}	Collector-Base breakdown voltage	I _c =-1mA; I _E = 0	-150			v
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = -5A; I _B = -0.5A			-1.0	V
V _{BE(on)}	Base-Emitter On Voltage	I _C = -5A; V _{CE} = -5V			-1.5	v
Ісво	Collector Cutoff Current	V _{CB} = -150V; I _E = 0			-100	μ Α
I _{CEO}	Collector Cutoff Current	V _{CE} = -110V; I _B = 0			-100	μ Α
I _{EBO}	Emitter Cutoff Current	V _{EB} = -5V; I _C = 0			-10	μ Α
h _{FE -1}	DC Current Gain	Ic= -1A; Vce= -5V	80			
h _{FE -2}	DC Current Gain	I _C = -5A; V _{CE} = -5V	20			
f⊤	Current-Gain—Bandwidth Product	I _c =-0.5A ; V _{CE} = -10V		10		MHz

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