

**isc Silicon PNP Power Transistor**
**2SB553**
**DESCRIPTION**

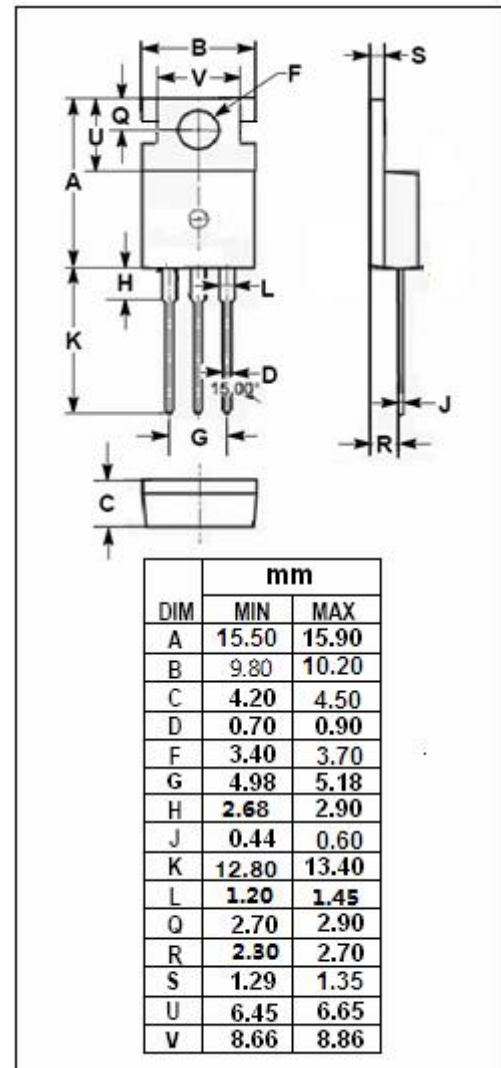
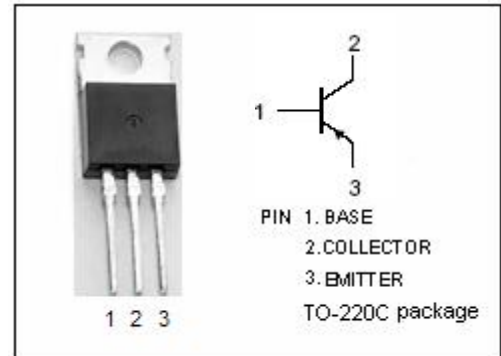
- Low Collector Saturation Voltage  
:  $V_{CE(sat)} = -0.4(V)(Max) @ I_C = -4A$
- Complement to Type 2SD553
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

**APPLICATIONS**

- High current switching applications.
- Power amplifier applications.

**ABSOLUTE MAXIMUM RATINGS ( $T_a = 25^\circ C$ )**

SYMBOL	PARAMETER	VALUE	UNIT
$V_{CBO}$	Collector-Base Voltage	-70	V
$V_{CEO}$	Collector-Emitter Voltage	-50	V
$V_{EBO}$	Emitter-Base Voltage	-5	V
$I_C$	Collector Current-Continuous	-7	A
$P_C$	Total Power Dissipation @ $T_a = 25^\circ C$	1.5	W
	Total Power Dissipation @ $T_C = 25^\circ C$	40	
$T_J$	Junction Temperature	150	$^\circ C$
$T_{stg}$	Storage Temperature Range	-55~150	$^\circ C$



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**ELECTRICAL CHARACTERISTICS**

 T<sub>c</sub>=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V <sub>(BR)CEO</sub>	Collector-Emitter Breakdown Voltage	I <sub>C</sub> = -50mA; I <sub>B</sub> = 0	-50			V
V <sub>CE(sat)</sub>	Collector-Emitter Saturation Voltage	I <sub>C</sub> = -4A; I <sub>B</sub> = -0.4A			-0.4	V
V <sub>BE(sat)</sub>	Base-Emitter Saturation Voltage	I <sub>C</sub> = -4A; I <sub>B</sub> = -0.4A			-1.2	V
I <sub>CBO</sub>	Collector Cutoff Current	V <sub>CB</sub> = -70V ; I <sub>E</sub> = 0			-30	μ A
I <sub>EBO</sub>	Emitter Cutoff Current	V <sub>EB</sub> = -5V; I <sub>C</sub> = 0			-50	μ A
h <sub>FE-1</sub>	DC Current Gain	I <sub>C</sub> = -1A ; V <sub>CE</sub> = -1V	70		240	
h <sub>FE-2</sub>	DC Current Gain	I <sub>C</sub> = -4A ; V <sub>CE</sub> = -1V	30			
f <sub>T</sub>	Current-Gain—Bandwidth Product	I <sub>C</sub> = -1A ; V <sub>CE</sub> = -4V		10		MHz
C <sub>OB</sub>	Output Capacitance	I <sub>E</sub> = 0; V <sub>CB</sub> = -10V; f <sub>test</sub> = 1MHz		250		pF

**◆ h<sub>FE-1</sub> Classifications**

O	Y
70-140	120-240

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