

**isc Silicon PNP Power Transistor**
**2SB567**
**DESCRIPTION**

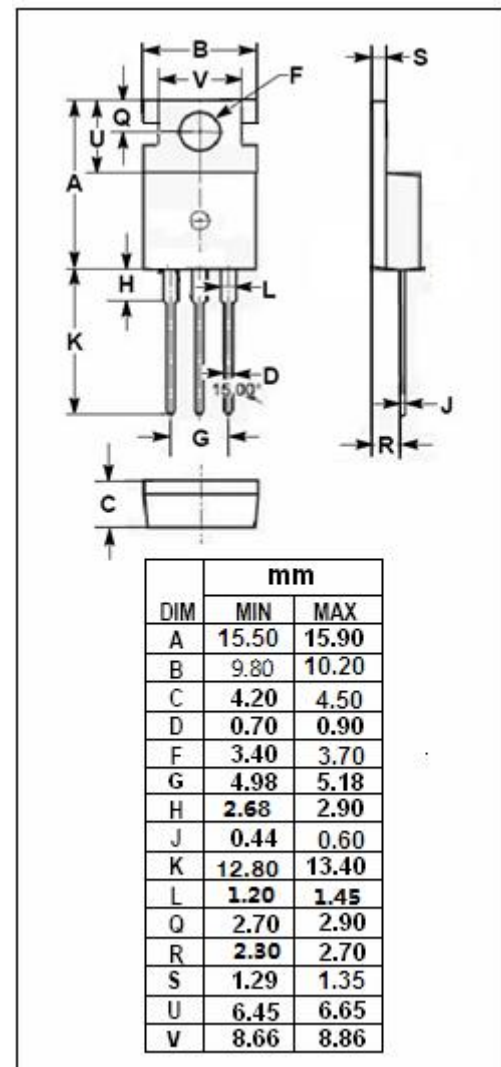
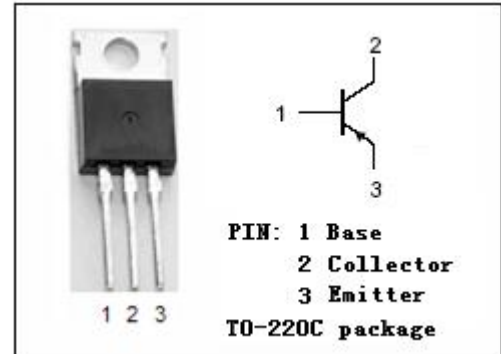
- Collector-Emitter Breakdown Voltage-  
:  $V_{(BR)CEO} = -150V(\text{Min.})$
- Low Collector Saturation Voltage-  
:  $V_{CE(sat)} = -2.0(\text{Max.}) @ I_C = -0.5A$
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

**APPLICATIONS**

- Designed for TV vertical deflection output applications.

**ABSOLUTE MAXIMUM RATINGS ( $T_a = 25^\circ\text{C}$ )**

SYMBOL	PARAMETER	VALUE	UNIT
$V_{CBO}$	Collector-Base Voltage	-200	V
$V_{CEO}$	Collector-Emitter Voltage	-150	V
$V_{EBO}$	Emitter-Base Voltage	-6	V
$I_C$	Collector Current-Continuous	-2	A
$I_{CM}$	Collector Current-Peak	-5	A
$P_C$	Collector Power Dissipation@ $T_c=25^\circ\text{C}$	30	W
	Collector Power Dissipation@ $T_a=25^\circ\text{C}$	1.8	
$T_J$	Junction Temperature	150	$^\circ\text{C}$
$T_{stg}$	Storage Temperature	-45~150	$^\circ\text{C}$



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**ELECTRICAL CHARACTERISTICS**

 T<sub>j</sub>=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V <sub>(BR)CEO</sub>	Collector-Emitter Breakdown Voltage	I <sub>C</sub> = -50mA; R <sub>BE</sub> = ∞	-150			V
V <sub>(BR)EBO</sub>	Emitter-Base Breakdown Voltage	I <sub>E</sub> = -5mA; I <sub>C</sub> = 0	-6			V
V <sub>CE(sat)</sub>	Collector-Emitter Saturation Voltage	I <sub>C</sub> = -0.5A; I <sub>B</sub> = -50mA			-2.0	V
V <sub>BE(on)</sub>	Base-Emitter On Voltage	I <sub>C</sub> = -50mA; V <sub>CE</sub> = -4V			-1.0	V
I <sub>CBO</sub>	Collector Cutoff Current	V <sub>CB</sub> = -120V; I <sub>E</sub> = 0			-1.0	μ A
h <sub>FE-1</sub>	DC Current Gain	I <sub>C</sub> = -50mA; V <sub>CE</sub> = -4V	60		320	
h <sub>FE-2</sub>	DC Current Gain	I <sub>C</sub> = -0.5A; V <sub>CE</sub> = -4V	60			
C <sub>OB</sub>	Output Capacitance	I <sub>E</sub> = 0; V <sub>CB</sub> = -10V; f= 1MHz		22		pF

**◆ h<sub>FE-1</sub> Classifications**

B	C	D
60-120	100-200	160-320

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