

isc Silicon PNP Power Transistor

2SB724

DESCRIPTION

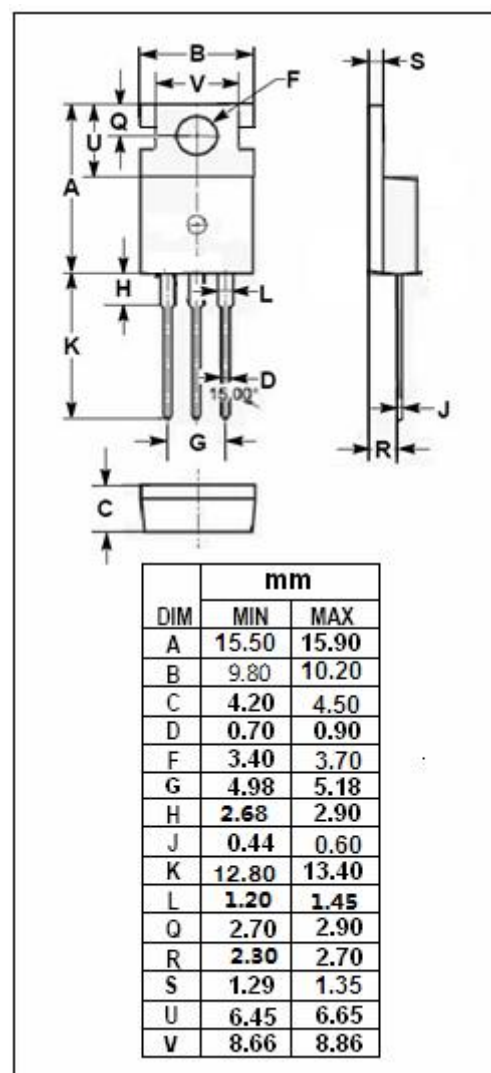
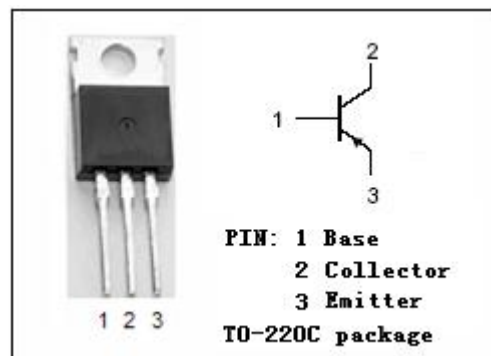
- Collector-Emitter Breakdown Voltage-
: $V_{(BR)CEO} = -60V(\text{Min})$
- Good Linearity of h_{FE}
- Fast Switching Speed
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Designed for use in general purpose power amplifier and switching applications

ABSOLUTE MAXIMUM RATINGS($T_a=25^\circ\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	-100	V
V_{CEO}	Collector-Emitter Voltage	-60	V
V_{EBO}	Emitter-Base Voltage	-5	V
I_C	Collector Current-Continuous	-3	A
I_{CM}	Collector Current-Peak	-5	A
P_C	Collector Power Dissipation @ $T_C=25^\circ\text{C}$	25	W
T_J	Junction Temperature	150	$^\circ\text{C}$
T_{stg}	Storage Temperature Range	-55~150	$^\circ\text{C}$



isc Silicon PNP Power Transistor**2SB724****ELECTRICAL CHARACTERISTICS****T_c=25°C unless otherwise specified**

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = -30mA ; I _B = 0	-60			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = -3A ; I _B = -0.6A			-1.2	V
V _{BE(on)}	Base-Emitter On Voltage	I _C = -3A ; V _{CE} = -4V			-1.8	V
I _{CBO}	Collector Cutoff Current	V _{CB} = -100V ; I _E = 0			-20	μ A
I _{EBO}	Emitter Cutoff Current	V _{EB} = -5V ; I _C = 0			-10	μ A
h _{FE1}	DC Current Gain	I _C = -1A ; V _{CE} = -3V	25			
h _{FE2}	DC Current Gain	I _C = -3A ; V _{CE} = -3V	10			
f _T	Current-Gain—Bandwidth Product	I _C =-0.5A ; V _{CE} = -10V	5			MHz

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