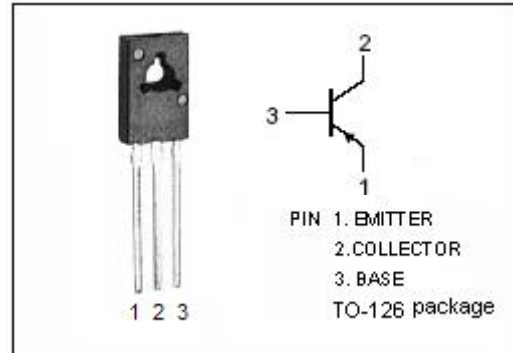


isc Silicon PNP Power Transistor
2SB731
DESCRIPTION

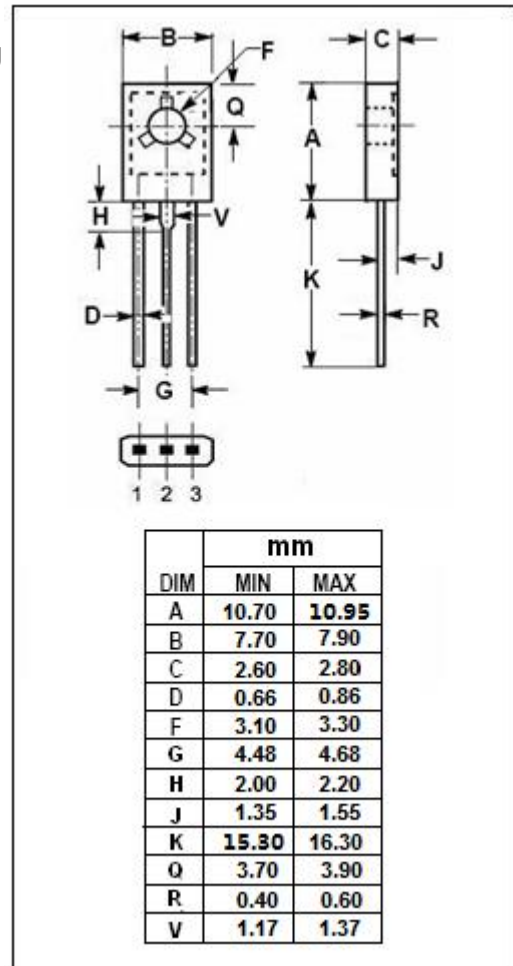
- Collector-Emitter Sustaining Voltage -
: $V_{CE(SUS)} = -50V(\text{Min})$
- Low Collector to Emitter Saturation Voltage
: $V_{CE(sat)} = -0.6V(\text{Max.})@I_C = -1A$
- DC Current Gain-
: $h_{FE} = 135-600@I_C = -0.1A$
- Minimum Lot-to-Lot variations for robust device performance and reliable operation


APPLICATIONS

- Designed for audio frequency power and low speed switching applications.

ABSOLUTE MAXIMUM RATINGS($T_a=25^\circ\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	-60	V
V_{CEO}	Collector-Emitter Voltage	-50	V
V_{EBO}	Emitter-Base Voltage	-6	V
I_C	Collector Current-Continuous	-1	A
I_{CM}	Collector Current-Pulse	-2	A
I_B	Base Current-Continuous	-0.5	A
P_C	Collector Power Dissipation @ $T_C=25^\circ\text{C}$	10	W
T_J	Junction Temperature	150	$^\circ\text{C}$
T_{stg}	Storage Temperature Range	-55~150	$^\circ\text{C}$



isc Silicon PNP Power Transistor
2SB731
ELECTRICAL CHARACTERISTICS
 $T_C=25^{\circ}\text{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
$V_{CE(SUS)}$	Collector-Emitter Sustaining Voltage	$I_C = -10\text{mA}; I_B = 0$	-50			V
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	$I_C = -1\text{A}; I_B = -50\text{mA}$			-0.6	V
$V_{BE(sat)}$	Base-Emitter Saturation Voltage	$I_C = -1\text{A}; I_B = -50\text{mA}$			-1.2	V
I_{CBO}	Collector Cutoff Current	$V_{CB} = -50\text{V}; I_E = 0$			-0.1	μA
I_{EBO}	Emitter Cutoff Current	$V_{EB} = -6\text{V}; I_C = 0$			-0.1	μA
h_{FE-1}	DC Current Gain	$I_C = -0.1\text{A}; V_{CE} = -2\text{V}$	135		600	
h_{FE-2}	DC Current Gain	$I_C = -1\text{A}; V_{CE} = -1\text{V}$	40			
C_{OB}	Output Capacitance	$I_E = 0; V_{CB} = -10\text{V}; f_{test} = 1.0\text{MHz}$		25		pF
f_T	Current-Gain—Bandwidth Product	$I_C = -10\text{mA}; V_{CE} = -2\text{V}$		75		MHz

 Pulsed: Pulse duration $\leq 350\ \mu\text{s}$, duty cycle $\leq 2\%$
 h_{FE-1} Classifications

L	K	F	E
135-270	200-400	300-480	360-600

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