

isc Silicon PNP Power Transistor

2SB782

DESCRIPTION

- Collector-Emitter Breakdown Voltage-
 - : V_{(BR)CEO}= -60V(Min)
- · Good Linearity of hFE
- · Fast Switching Speed
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

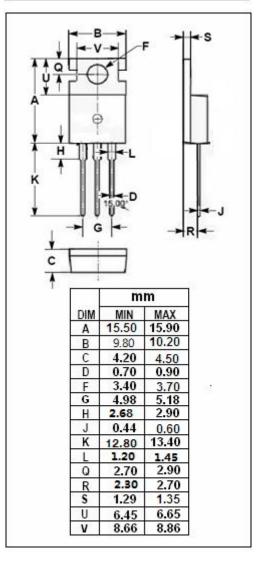
PIN: 1 Base 2 Collector 3 Emitter T0-220C package

APPLICATIONS

 Designed for use in general purpose power amplifier and switching applications

ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

| SYMBOL | PARAMETER | VALUE | UNIT |
|------------------|--|---------|------------|
| V _{CBO} | Collector-Base Voltage | -60 | V |
| V _{CEO} | Collector-Emitter Voltage | -60 | V |
| V _{EBO} | Emitter-Base Voltage | -5 | V |
| Ic | Collector Current-Continuous | -4 | А |
| I _{CM} | Collector Current-Peak | -6 | А |
| Pc | Collector Power Dissipation @ T _C =25°C | 30 | W |
| TJ | Junction Temperature | 150 | $^{\circ}$ |
| T _{stg} | Storage Temperature Range | -55~150 | $^{\circ}$ |



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ELECTRICAL CHARACTERISTICS

T_C=25℃ unless otherwise specified

| SYMBOL | PARAMETER | CONDITIONS | MIN | TYP. | MAX | UNIT |
|-----------------------|--------------------------------------|--|-----|------|------|------|
| V _(BR) CEO | Collector-Emitter Breakdown Voltage | I _C = -10mA ; I _B = 0 | -60 | | | V |
| V _{CE(sat)} | Collector-Emitter Saturation Voltage | I _C = -4A; I _B = -0.4A | | | -1.0 | V |
| V _{BE(on)} | Base-Emitter On Voltage | I _C = -0.5A ; V _{CE} = -5V | | | -1.0 | V |
| I _{CBO} | Collector Cutoff Current | V _{CB} = -60V ; I _E = 0 | | | -100 | μА |
| I _{EBO} | Emitter Cutoff Current | V _{EB} = -5V; I _C = 0 | | | -10 | μА |
| h _{FE1} | DC Current Gain | Ic= -0.5A; V _{CE} = -5V | 60 | | 320 | |
| h _{FE2} | DC Current Gain | I _C = -4A ; V _{CE} = -5V | 10 | | | |
| f⊤ | Current-Gain—Bandwidth Product | I _C =-0.5A; V _{CE} = -10V | 5 | | | MHz |

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