

isc Silicon PNP Power Transistor

DESCRIPTION

- · Collector-Emitter Sustaining Voltage-
 - : V_{CEO(SUS)}= -200V(Min)
- · Low Collector Saturation Voltage-
- : V_{CE(sat)}= -1.0V(Max.) @I_C= -5A
- · Wide area of safe operation
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

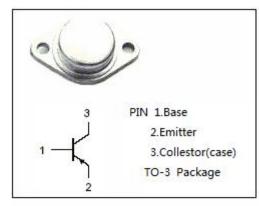


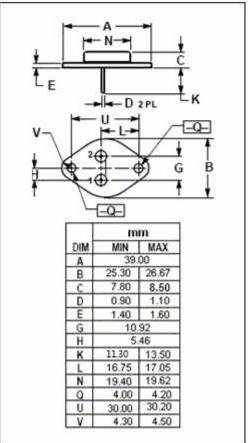
APPLICATIONS

Designed for general-purpose switching and amplifier applications



SYMBOL	PARAMETER	VALUE	UNIT	
V_{CBO}	Collector-Base Voltage	-200	V	
Vceo	Collector-Emitter Voltage	-200	V	
V _{EBO}	Emitter-Base Voltage	-5	V	
Ic	Collector Current-Continuous	-10	Α	
Pc	Collector Power Dissipation @Tc=25℃	20	W	
TJ	Junction Temperature	150	$^{\circ}$ C	
T _{stg}	Storage Temperature	-55~150	$^{\circ}$	







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2SB796

ELECTRICAL CHARACTERISTICS

Tj=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	МАХ	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = -10mA; I _B = 0	-200			V
V _{(BR)EBO}	Emitter-Base Breakdown Voltage	I _E = -1mA; I _C = 0	-5			V
V _{(BR)CBO}	Collector-Base breakdown voltage	I _C =-1mA; I _E = 0	-200			V
V _{CE} (sat)	Collector-Emitter Saturation Voltage	I _C = -5A; I _B = -0.5A			-1.0	V
V _{BE(on)}	Base-Emitter On Voltage	I _C = -1A; V _{CE} = -5V			-1.5	V
Ісво	Collector Cutoff Current	V _{CB} = -200V; I _E = 0			-100	μА
I _{CEO}	Collector Cutoff Current	V _{CE} = -200V; I _B = 0			-100	μА
I _{EBO}	Emitter Cutoff Current	V _{EB} = -5V; I _C = 0			-10	μА
h _{FE} -1	DC Current Gain	I _C = -2A; V _{CE} = -5V	40		200	
h _{FE -2}	DC Current Gain	I _C = -5A; V _{CE} = -5V	10			
f _T	Current-Gain—Bandwidth Product	I _C =-0.5A ; V _{CE} = -10V		15		MHz

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