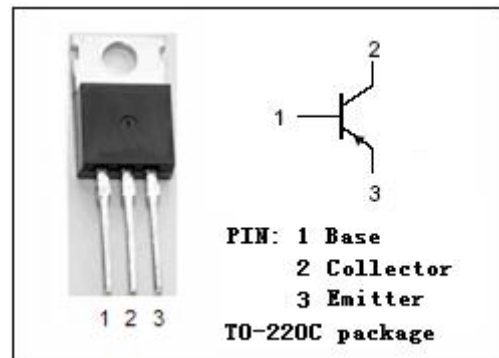


**isc Silicon PNP Power Transistor**
**2SB813**
**DESCRIPTION**

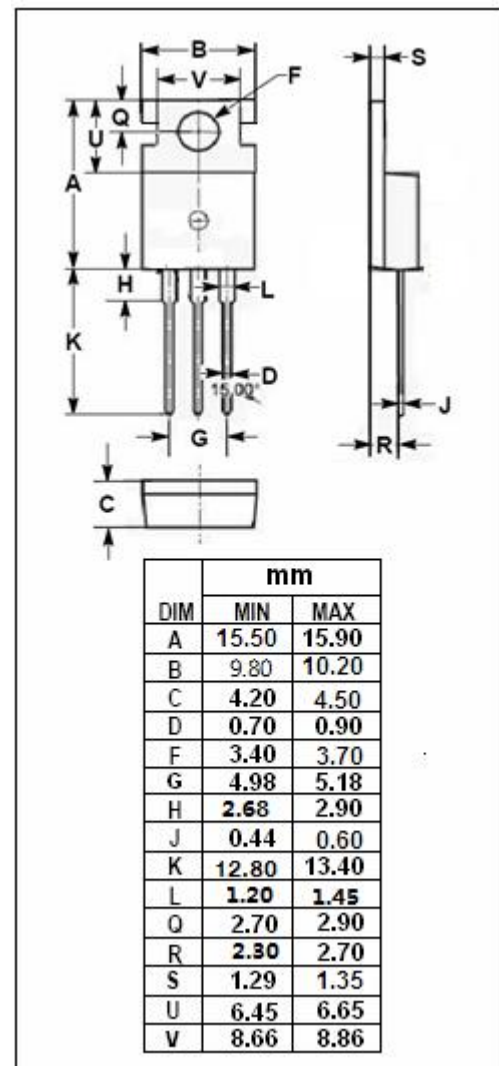
- Collector-Emitter Breakdown Voltage-  
:  $V_{(BR)CEO} = -60V(\text{Min})$
- Good Linearity of  $h_{FE}$
- High Power Dissipation
- Minimum Lot-to-Lot variations for robust device performance and reliable operation


**APPLICATIONS**

- Designed for AF power amplifier applications

**ABSOLUTE MAXIMUM RATINGS( $T_a=25^\circ\text{C}$ )**

SYMBOL	PARAMETER	VALUE	UNIT
$V_{CBO}$	Collector-Base Voltage	-60	V
$V_{CEO}$	Collector-Emitter Voltage	-60	V
$V_{EBO}$	Emitter-Base Voltage	-5	V
$I_C$	Collector Current-Continuous	-4	A
$I_{CM}$	Collector Current-Peak	-8	A
$P_C$	Collector Power Dissipation @ $T_C=25^\circ\text{C}$	40	W
$T_J$	Junction Temperature	150	$^\circ\text{C}$
$T_{stg}$	Storage Temperature Range	-55~150	$^\circ\text{C}$



**isc Silicon PNP Power Transistor****2SB813****ELECTRICAL CHARACTERISTICS** $T_C=25^{\circ}\text{C}$  unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
$V_{(BR)CEO}$	Collector-Emitter Breakdown Voltage	$I_C = -5\text{mA}; I_B = 0$	-60			V
$V_{(BR)CBO}$	Collector-Base Breakdown Voltage	$I_C = -1\text{mA}; I_E = 0$	-60			V
$V_{(BR)EBO}$	Emitter-Base Breakdown Voltage	$I_E = -1\text{mA}; I_C = 0$	-5			V
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	$I_C = -4\text{A}; I_B = -0.4\text{A}$			-1.5	V
$I_{CEO}$	Collector Cutoff Current	$V_{CE} = -40\text{V}; I_B = 0$			-100	$\mu\text{A}$
$I_{EBO}$	Emitter Cutoff Current	$V_{EB} = -4\text{V}; I_C = 0$			-100	$\mu\text{A}$
$h_{FE}$	DC Current Gain	$I_C = -1\text{A}; V_{CE} = -2\text{V}$	60		200	
$f_T$	Current-Gain—Bandwidth Product	$I_C = -0.1\text{A}; V_{CE} = -5\text{V}$		20		MHz

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