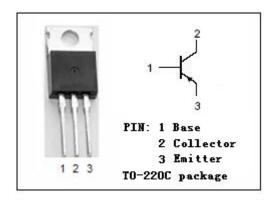


# isc Silicon PNP Power Transistor

#### **DESCRIPTION**

- Collector Current: I<sub>C</sub>= -3A
- Low Collector Saturation Voltage
  - : V<sub>CE(sat)</sub>= -1.2V(Max)@I<sub>C</sub>= -2A
- · High Collector Power Dissipation
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

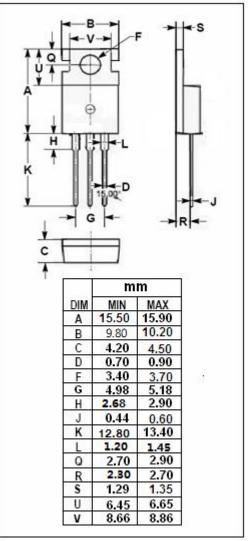


#### **APPLICATIONS**

• Designed for low frequency power amplifier applications.

# ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER	VALUE	UNIT
V <sub>CBO</sub>	Collector-Base Voltage	-50	V
Vceo	Collector-Emitter Voltage	-50	V
V <sub>EBO</sub>	Emitter-Base Voltage	-4	V
Ic	Collector Current-Continuous	-3	А
Pc	Total Power Dissipation @ T <sub>C</sub> =25℃	25	W
TJ	Junction Temperature	150	°C
T <sub>stg</sub>	Storage Temperature Range	-45~150	$^{\circ}$





## isc Silicon PNP Power Transistor

2SB856

### **ELECTRICAL CHARACTERISTICS**

T<sub>C</sub>=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V <sub>(BR)CEO</sub>	Collector-Emitter Breakdown Voltage	I <sub>C</sub> = -30mA ; R <sub>BE</sub> = ∞	-50			V
V <sub>(BR)CBO</sub>	Collector-Base Breakdown Voltage	I <sub>C</sub> = -5mA ; I <sub>E</sub> = 0	-50			V
V <sub>(BR)EBO</sub>	Emitter-Base Breakdown Voltage	I <sub>E</sub> = -5mA; I <sub>C</sub> = 0	-4			V
V <sub>CE(sat)</sub>	Collector-Emitter Saturation Voltage	I <sub>C</sub> = -2A; I <sub>B</sub> = -0.2A			-1.2	V
V <sub>BE</sub> (on)	Base-Emitter On Voltage	I <sub>C</sub> = -1A; V <sub>CE</sub> = -4V			-1.5	V
I <sub>CBO</sub>	Collector Cutoff Current	V <sub>CB</sub> = -20V; I <sub>E</sub> = 0			-100	μА
h <sub>FE-1</sub>	DC Current Gain	I <sub>C</sub> = -1A; V <sub>CE</sub> = -4V	35		200	
h <sub>FE-2</sub>	DC Current Gain	I <sub>C</sub> = -0.1A; V <sub>CE</sub> = -4V	35			
f⊤	Current-Gain—Bandwidth Product	I <sub>C</sub> = -0.5A; V <sub>CE</sub> = -4V		35		MHz

## ♦ h<sub>FE-1</sub> Classifications

Α	В	С
35-70	60-120	100-200

### **NOTICE:**

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