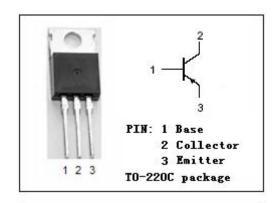


isc Silicon PNP Power Transistor

DESCRIPTION

- Collector Current: I_C= -4A
- · Low Collector Saturation Voltage
 - : $V_{CE(sat)}$ = -1.0V(Max)@I_C= -2A
- · High Collector Power Dissipation
- · Complement to Type 2SD1134
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

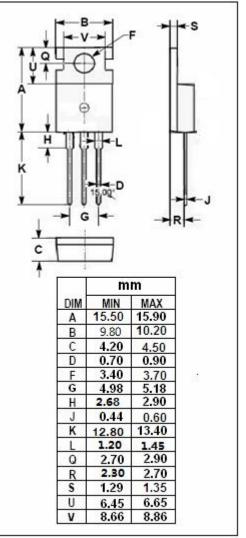


APPLICATIONS

• Designed for low frequency power amplifier applications.

ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER	VALUE	UNIT
V _{CBO}	Collector-Base Voltage	-70	V
Vceo	Collector-Emitter Voltage	-60	V
V _{EBO}	Emitter-Base Voltage	-5	V
lc	Collector Current-Continuous	-4	А
Ісм	Collector Current-Peak	-8	А
Pc	Total Power Dissipation @ T _C =25℃	40	W
Тл	Junction Temperature	150	$^{\circ}$
T _{stg}	Storage Temperature Range	-45~150	$^{\circ}$





isc Silicon PNP Power Transistor

2SB858

ELECTRICAL CHARACTERISTICS

 $T_c=25$ °C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = -30mA ; R _{BE} = ∞	-60			V
V _{(BR)CBO}	Collector-Base Breakdown Voltage	I _C = -10 μ A ; I _E = 0	-70			V
V _{(BR)EBO}	Emitter-Base Breakdown Voltage	I _E = -10 μ A ; I _C = 0	-5			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = -2A; I _B = -0.2A			-1.0	V
V _{BE} (on)	Base-Emitter On Voltage	I _C = -1A ; V _{CE} = -4V			-1.0	V
Ісво	Collector Cutoff Current	V _{CB} = -50V ; I _E = 0			-1	μА
h _{FE-1}	DC Current Gain	I _C = -1A ; V _{CE} = -4V	60		320	
h _{FE-2}	DC Current Gain	I _C = -0.1A; V _{CE} = -4V	35			
f⊤	Current-Gain—Bandwidth Product	I _C = -0.5A; V _{CE} = -4V		15		MHz

h_{FE-1} Classifications

В	С	D
60-120	100-200	160-320

NOTICE:

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