

## **isc Silicon PNP Power Transistor**

# 2SB900

## DESCRIPTION

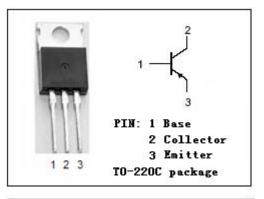
- Collector-Emitter BreakdownVoltage : V<sub>(BR)CEO</sub>= -50V(Min.)
- Low Collector Saturation Voltage-
- : V<sub>CE(sat)</sub>= -1.0(Max.) @I<sub>C</sub>= -2A
- Wide area of safe operation
- Good Linearity of  $h_{\text{FE}}$
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

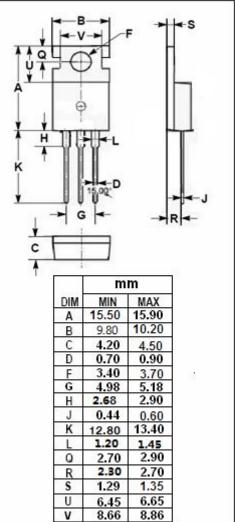
### APPLICATIONS

• Designed for power amplifier and switching applications .

## ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER	VALUE	UNIT
V <sub>CBO</sub>	Collector-Base Voltage	-50	V
V <sub>CEO</sub>	Collector-Emitter Voltage	-50	V
V <sub>EBO</sub>	Emitter-Base Voltage	-6	V
lc	Collector Current-Continuous	-4	А
I <sub>СМ</sub>	Collector Current-Peak	-6	A
Pc	Collector Power Dissipation@Tc=25°C	40	W
TJ	Junction Temperature	150	°C
T <sub>stg</sub>	Storage Temperature	-55~150	°C







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## **ELECTRICAL CHARACTERISTICS**

#### Tj=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	МАХ	UNIT
V(BR)CEO	Collector-Emitter Breakdown Voltage	I <sub>C</sub> = -1mA; I <sub>B</sub> = 0	-50			V
V <sub>(BR)CBO</sub>	Collector-Base breakdown voltage	I <sub>C</sub> =-1mA; I <sub>E</sub> = 0	-50			V
V <sub>(BR)EBO</sub>	Emitter-Base Breakdown Voltage	I <sub>E</sub> = -1mA; I <sub>C</sub> = 0	-6			V
V <sub>CE(sat)</sub>	Collector-Emitter Saturation Voltage	I <sub>C</sub> = -2A; I <sub>B</sub> = -0.2A			-1.0	V
V <sub>BE(on)</sub>	Base-Emitter On Voltage	I <sub>C</sub> =- 2A; V <sub>CE</sub> =-4V			-1.4	V
Ісво	Collector Cutoff Current	V <sub>CB</sub> = -50V; I <sub>E</sub> = 0			-100	μA
I <sub>CEO</sub>	Collector Cutoff Current	V <sub>CE</sub> = -50V; I <sub>B</sub> = 0			-100	μA
I <sub>EBO</sub>	Emitter Cutoff Current	V <sub>EB</sub> = -5V; I <sub>C</sub> = 0			-10	μA
h <sub>FE</sub>	DC Current Gain	I <sub>C</sub> = -1A ; V <sub>CE</sub> = -4V	40		200	
f⊤	Current-Gain—Bandwidth Product	I <sub>C</sub> =-0.5A ; V <sub>CE</sub> = -10V	6			MHz

### Notice:

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