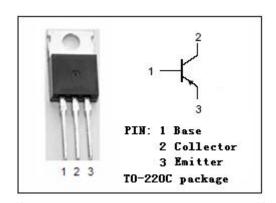


isc Silicon PNP Power Transistor

DESCRIPTION

- High Collector Current:: I_C= -7A
- Low Collector Saturation Voltage
 - : $V_{CE(sat)}$ = -0.6V(Max)@I_C= -5A
- · High Speed Switching
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

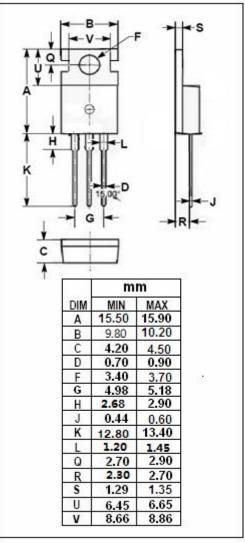


APPLICATIONS

• Designed for low voltage switching applications.

ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER	VALUE	UNIT
V _{CBO}	Collector-Base Voltage	-40	V
V _{CEO}	Collector-Emitter Voltage	-20	V
V_{EBO}	Emitter-Base Voltage	-5	V
Ic	Collector Current-Continuous	-7	А
I _{CM}	Collector Current-Peak	-12	А
Pc	Total Power Dissipation @ T _C =25℃	30	W
TJ	Junction Temperature	150	$^{\circ}$
T _{stg}	Storage Temperature Range	-55~150	$^{\circ}$





isc Silicon PNP Power Transistor

2SB925

ELECTRICAL CHARACTERISTICS

 $T_c=25$ °C unless otherwise specified

				1	1	
SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = -10mA; I _B = 0	-20			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = -5A; I _B = -0.16A			-0.6	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = -5A; I _B = -0.16A			-1.5	V
I _{CBO}	Collector Cutoff Current	V _{CB} = -40V; I _E = 0			-50	μА
I _{EBO}	Emitter Cutoff Current	V _{EB} = -5V; I _C = 0			-50	μА
h _{FE-1}	DC Current Gain	Ic= -0.1A; Vc== -2V	45			
h _{FE-2}	DC Current Gain	I _C = -2A; V _{CE} = -2V	60		260	
Сов	Collector Output Capacitance	I _E = 0; V _{CB} = -10V; f= 1MHz		140		pF
f⊤	Current-Gain—Bandwidth Product	Ic= -0.5A; VcE= -10V		150		MHz

♦ h_{FE-2} Classifications

R	Q	Р
60-120	90-180	130-260

NOTICE:

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