

isc Silicon PNP Power Transistor

DESCRIPTION

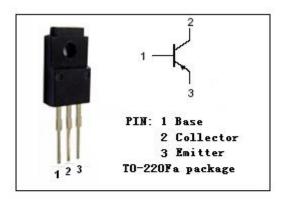
- · Low Collector Saturation Voltage
 - : $V_{CE(sat)}$ = -0.5V(Max)@ I_C = -3A
- · Collector-Emitter Breakdown Voltage-
 - $: V_{(BR)CEO} = -80V (Min)$
- · Good Linearity of hFE
- Complement to Type 2SD1269
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

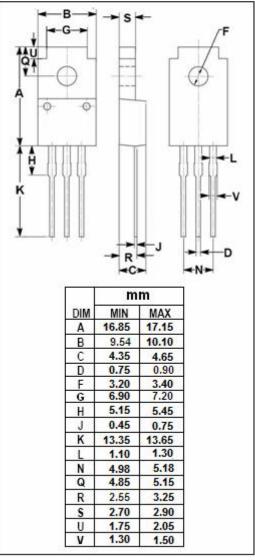
APPLICATIONS

• Designed for power amplification.

ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER	VALUE	UNIT	
V _{CBO}	Collector-Base Voltage	-130	V	
V _{CEO}	Collector-Emitter Voltage	-80	٧	
V _{EBO}	Emitter-Base Voltage	-7	V	
lc	Collector Current-Continuous -4		А	
Ісм	Collector Current-Peak	-8	Α	
P _C	Collector Power Dissipation @ T _C =25℃	35	W	
	Collector Power Dissipation @ T _a =25℃	2		
TJ	Junction Temperature 150 °C		\mathbb{C}	
T _{stg}	Storage Temperature Range -		$^{\circ}$	







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2SB944

ELECTRICAL CHARACTERISTICS

T_c=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = -10mA ; I _B = 0	-80			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = -3A; I _B = -0.15A			-0.5	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = -3A; I _B = -0.15A			-1.5	V
Ісво	Collector Cutoff Current	V _{CB} = -100V; I _E = 0			-10	μА
I _{EBO}	Emitter Cutoff Current	V _{EB} = -5V; I _C = 0			-50	μА
h _{FE-1}	DC Current Gain	I _C = -0.1A; V _{CE} = -2V	45			
h _{FE-2}	DC Current Gain	I _C = -1A; V _{CE} = -2V	90		260	

♦ h_{FE-2} classifications

Q	Р
90-180	130-260

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