

isc Silicon PNP Power Transistor

2SB947

DESCRIPTION

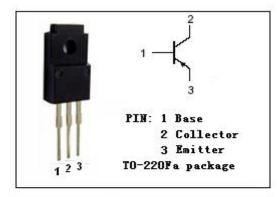
- Low Collector Saturation Voltage-
 - : $V_{CE(sat)} = -0.6V(Max)@I_C = -7A$
- · High Speed Switching
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

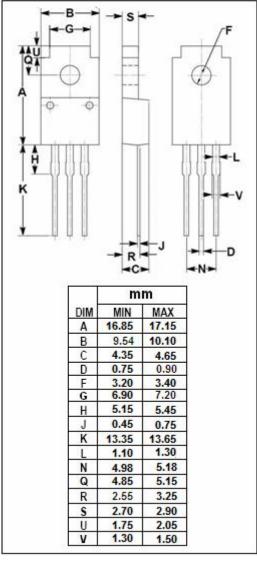
APPLICATIONS

• Designed for low-voltage switching applications.

ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER	VALUE	UNIT	
V _{СВО}	Collector-Base Voltage	-40	V	
V _{CEO}	Collector-Emitter Voltage	-20	V	
V _{EBO}	Emitter-Base Voltage	-5	V	
Ic	Collector Current-Continuous -10		А	
Ісм	Collector Current-Peak	nt-Peak -15 A		
Pc	Collector Power Dissipation @ T _a =25℃	2	W	
	Collector Power Dissipation @ Tc=25℃	35		
TJ	Junction Temperature	150	°C	
T _{stg}	Storage Temperature Range	-55~150	°C	







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ELECTRICAL CHARACTERISTICS

T_C=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = -10mA; I _B = 0	-20			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = -7A; I _B = -0.23A			-0.6	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = -7A; I _B = -0.23A			-1.5	V
I _{CBO}	Collector Cutoff Current	V _{CB} = -40V; I _E = 0			-50	μА
I _{EBO}	Emitter Cutoff Current	V _{EB} = -5V; I _C = 0			-50	μА
h _{FE-1}	DC Current Gain	I _C = -0.1A; V _{CE} = -2V	45			
h _{FE-2}	DC Current Gain	I _C = -2A; V _{CE} = -2V	90		260	

♦ h_{FE-1} Classifications

Q	Р
90-180	130-260

NOTICE:

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