

isc Silicon PNP Power Transistor

DESCRIPTION

- · Low Collector Saturation Voltage-
 - : $V_{CE(sat)}$ = -0.6V(Max)@ I_C = -10A
- · High Speed Switching
- Complement to Type 2SD1445
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

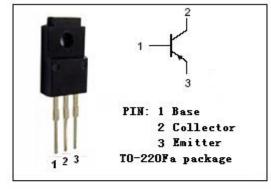


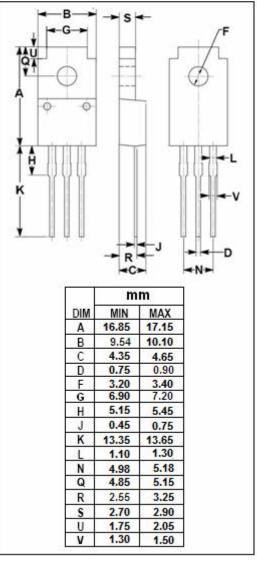
APPLICATIONS

Designed for low-voltage switching applications.

ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER	VALUE	UNIT	
V _{CBO}	Collector-Base Voltage	-40	V	
Vceo	Collector-Emitter Voltage	-20	V	
V _{EBO}	Emitter-Base Voltage	-5	V	
Ic	Collector Current-Continuous	-10	А	
Ісм	Collector Current-Peak -20		A	
D	Collector Power Dissipation @ T _a =25℃	2	W	
Pc	Collector Power Dissipation @ T _C =25℃	40		
Тл	Junction Temperature 150		°C	
T _{stg}	Storage Temperature Range	-55~150	°C	







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2SB948

ELECTRICAL CHARACTERISTICS

T_C=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = -10mA; I _B = 0	-20			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = -10A; I _B = -0.33A			-0.6	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = -10A; I _B = -0.33A			-1.5	V
I _{CBO}	Collector Cutoff Current	V _{CB} = -40V; I _E = 0			-50	μА
I _{EBO}	Emitter Cutoff Current	V _{EB} = -5V; I _C = 0			-50	μА
h _{FE-1}	DC Current Gain	I _C = -0.1A; V _{CE} = -2V	45			
h _{FE-2}	DC Current Gain	I _C = -3A; V _{CE} = -2V	90		260	
Сов	Collector Output Capacitance	I _E = 0; V _{CB} = -10V; f _{test} =1MHz		400		pF

♦ h_{FE-1} Classifications

Q	Р
90-180	130-260

NOTICE:

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