

# isc Silicon PNP Power Transistor

2SB979

### **DESCRIPTION**

- · Collector-Emitter Breakdown Voltage-
  - : V<sub>(BR)CEO</sub>= -100V(Min)
- · Good Linearity of hFE
- Wide Area of Safe Operation
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

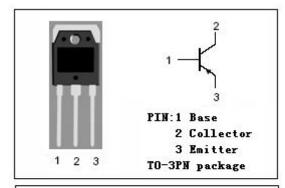


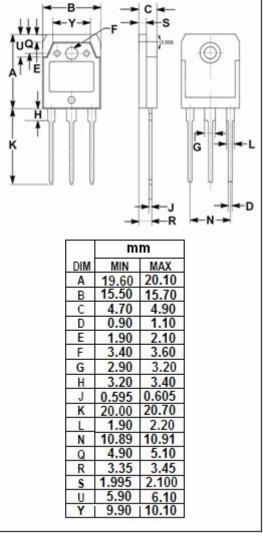
### **APPLICATIONS**

· Designed for high power amplifications.

ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER	VALUE	UNIT	
V <sub>CBO</sub>	Collector-Base Voltage	V		
V <sub>CEO</sub>	Collector-Emitter Voltage	-100	V	
V <sub>EBO</sub>	Emitter-Base Voltage	-5	V	
lc	Collector Current-Continuous	-5	Α	
I <sub>CP</sub>	Collector Current-Pulse	-8	Α	
P <sub>C</sub>	Collector Power Dissipation @ T <sub>C</sub> =25℃	60	W	
	Collector Power Dissipation @ T <sub>a</sub> =25℃	3		
TJ	Junction Temperature	150	$^{\circ}$	
T <sub>stg</sub>	Storage Temperature Range -55~150		$^{\circ}$	







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#### **ELECTRICAL CHARACTERISTICS**

T<sub>C</sub>=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V <sub>CE(sat)</sub>	Collector-Emitter Saturation Voltage	I <sub>C</sub> = -3A; I <sub>B</sub> = -0.3A			-2.0	V
V <sub>BE(on)</sub>	Base -Emitter On Voltage	I <sub>C</sub> = -3A; V <sub>CE</sub> = -5V			-1.8	V
I <sub>CBO</sub>	Collector Cutoff Current	V <sub>CB</sub> = -100V; I <sub>E</sub> = 0			-50	μА
I <sub>EBO</sub>	Emitter Cutoff Current	V <sub>EB</sub> = -3V; I <sub>C</sub> = 0			-50	μА
h <sub>FE-1</sub>	DC Current Gain	I <sub>C</sub> = -20mA; V <sub>CE</sub> = -5V	20			
h <sub>FE-2</sub>	DC Current Gain	I <sub>C</sub> = -1A; V <sub>CE</sub> = -5V	60		200	
h <sub>FE-3</sub>	DC Current Gain	I <sub>C</sub> = -3A; V <sub>CE</sub> = -5V	20			
f⊤	Current-Gain—Bandwidth Product	I <sub>C</sub> = -0.5A; V <sub>CE</sub> = -5 V; f= 1MHz		20		MHz

# ♦ h<sub>FE-2</sub>Classifications

Q	S	Р
60-120	80-160	100-200

## **NOTICE:**

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