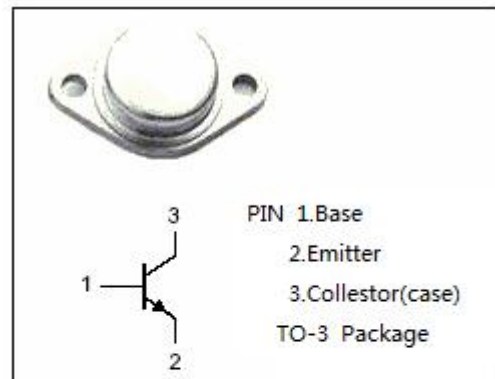


isc Silicon NPN Power Transistor
2SC1004
DESCRIPTION

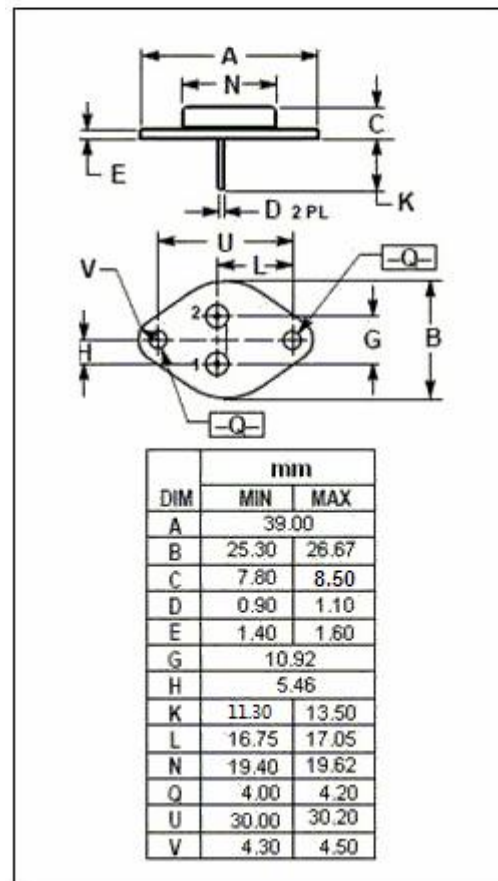
- With TO-3 Package
- High breakdown voltage
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- for use in horizontal deflection output stages for color TV receives


ABSOLUTE MAXIMUM RATINGS(T_a=25°C)

SYMBOL	PARAMETER	VALUE	UNIT
V _{CBO}	Collector-Base Voltage	1100	V
V _{CEO}	Collector-Emitter Voltage	700	V
V _{EBO}	Emitter-Base Voltage	5	V
I _C	Collector Current-Continuous	0.5	A
I _B	Base Current-Continuous	0.1	A
P _C	Collector Power Dissipation	50	W
T _J	Junction Temperature	150	°C
T _{stg}	Storage Temperature Range	-55~150	°C



isc Silicon NPN Power Transistor
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ELECTRICAL CHARACTERISTICS

 T_c=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = 150mA; I _B = 30mA			5.0	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = 150mA; I _B = 30mA			1.5	V
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = 10mA; I _B = 0	700			V
V _{(BR)EBO}	Emitter-Base Breakdown Voltage	I _E = 1mA; I _C = 0	5			V
I _{CBO}	Collector Cutoff Current	V _{CB} = 800V; I _E = 0			1	uA
I _{EBO}	Emitter Cutoff Current	V _{EB} = 4V; I _C = 0			1	uA
h _{FE}	DC Current Gain	I _C = 0.15A; V _{CE} = 15V	30		160	
f _T	Current-Gain—Bandwidth Product	I _C = 0.15A; V _{CE} = 15V		2		MHz

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