

isc Silicon NPN Power Transistor
2SC1050
DESCRIPTION

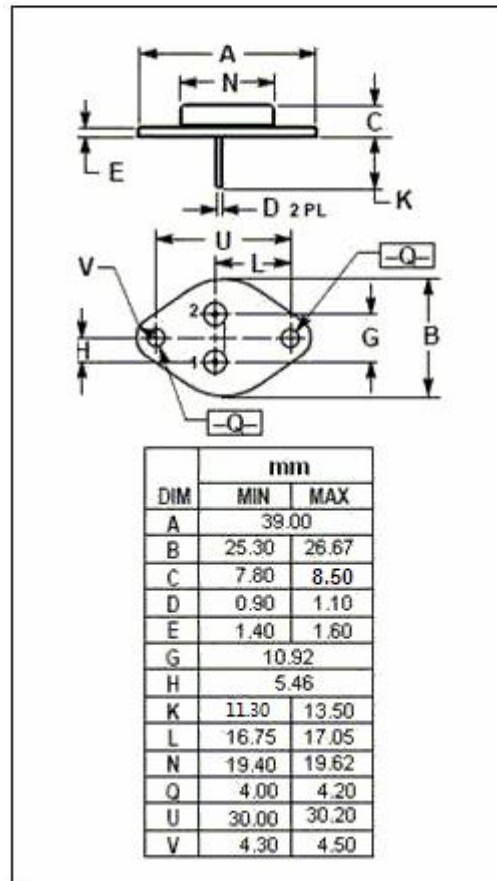
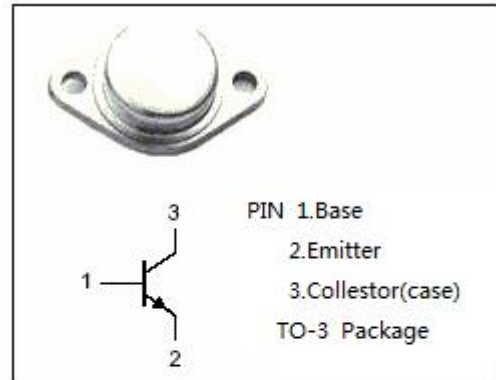
- With TO-3 Package
- High breakdown voltage
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Silicon NPN high frequency, high power transistors in a plastic envelope, primarily for use in audio and general purpose

ABSOLUTE MAXIMUM RATINGS (T_a=25°C)

SYMBOL	PARAMETER	VALUE	UNIT
V _{CBO}	Collector-Base Voltage	300	V
V _{CEO}	Collector-Emitter Voltage	250	V
V _{EBO}	Emitter-Base Voltage	5	V
I _C	Collector Current-Continuous	1.0	A
I _B	Base Current-Continuous	0.2	A
P _C	Collector Power Dissipation	40	W
T _J	Junction Temperature	150	°C
T _{stg}	Storage Temperature Range	-55~150	°C



isc Silicon NPN Power Transistor**2SC1050****ELECTRICAL CHARACTERISTICS**T_c=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = 0.5A; I _B = 100mA			2.0	V
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = 5mA; I _B = 0	250			V
V _{(BR)EBO}	Emitter-Base Breakdown Voltage	I _E = 1mA; I _C = 0	5			V
h _{FE}	DC Current Gain	I _C = 0.3A; V _{CE} = 5V	30		200	

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