

isc Silicon NPN Power Transistor

DESCRIPTION

- With TO-3 Package
- · Wide area of safe operation
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

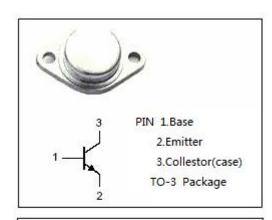


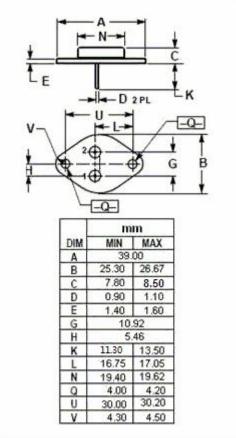
APPLICATIONS

· For audio frequency power amplifier applications

ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER	VALUE	UNIT	
V _{CBO}	Collector-Base Voltage	160	V	
V _{CEO}	Collector-Emitter Voltage	100	V	
V _{EBO}	Emitter-Base Voltage	5	V	
Ic	Collector Current-Continuous	6	А	
Pc	Collector Power Dissipation	50	W	
TJ	Junction Temperature	150	$^{\circ}$	
T _{stg}	Storage Temperature Range	-55~150	$^{\circ}$	







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2SC1112

ELECTRICAL CHARACTERISTICS

T_C=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{CE} (sat)	Collector-Emitter Saturation Voltage	Ic= 5A; I _B = 0.5A			2.0	V
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = 10mA; I _B = 0	100			V
V _{(BR)EBO}	Emitter-Base Breakdown Voltage	I _E = 1mA; I _C = 0	5			V
h _{FE}	DC Current Gain	I _C = 3.0A; V _{CE} = 4V	30		150	
fτ	Current-Gain—Bandwidth Product	I _C = 0.5A; V _{CE} = 12V; f _{test} = 1MHz	10			MHz



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