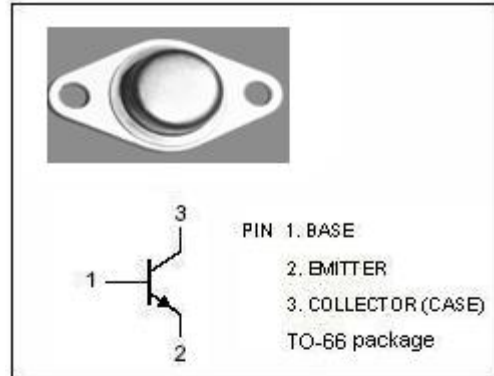


isc Silicon NPN Power Transistor
2SC1161
DESCRIPTION

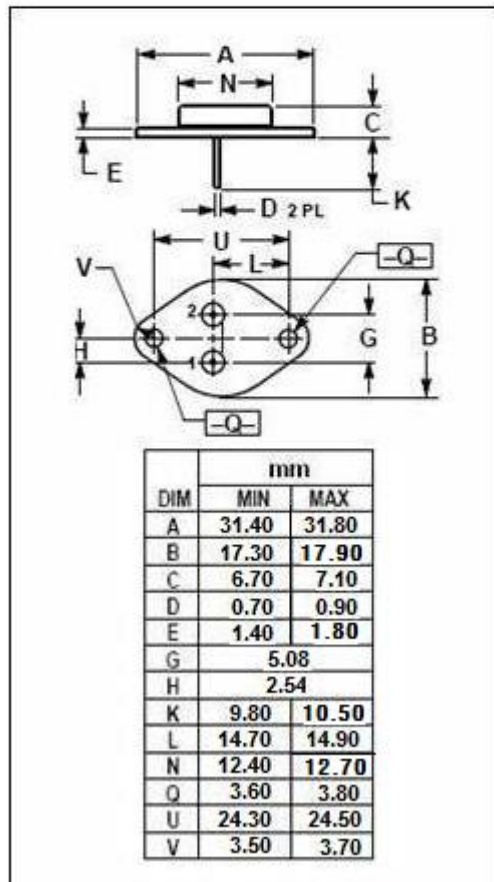
- With TO-66 Package
- Low collector saturation voltage
- Minimum Lot-to-Lot variations for robust device performance and reliable operation.

APPLICATIONS

- Designed for low frequency high voltage power amplifier TV vertical deflection output applications


ABSOLUTE MAXIMUM RATINGS(T_a=25°C)

SYMBOL	PARAMETER	VALUE	UNIT
V _{CB0}	Collector-Base Voltage	200	V
V _{CEO}	Collector-Emitter Voltage	120	V
V _{EB0}	Emitter-Base Voltage	6	V
I _c	Collector Current-Continuous	1.0	A
P _c	Collector Power Dissipation @ T _c =25°C	15	W
T _J	Junction Temperature	150	°C
T _{stg}	Storage Temperature Range	-55~150	°C


THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
R _{th j-c}	Thermal Resistance, Junction to Case	8.33	°C/W

isc Silicon NPN Power Transistor

2SC1161

ELECTRICAL CHARACTERISTICS

T_C=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	MAX	UNIT
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = 0.5A; I _B = 50mA		1.5	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = 0.5A; I _B = 50mA		2.0	V
I _{CBO}	Collector Cutoff Current	V _{CB} = 120V; I _B = 0		1.0	μA
I _{EBO}	Emitter Cutoff Current	V _{EB} = 6V; I _C =0		1.0	μA
h _{FE}	DC Current Gain	I _C = 0.2A ; V _{CE} = 5V	30	200	
f _T	Current-Gain—Bandwidth Product	I _C = 0.2A ; V _{CE} = 10V, f _{test} = 1.0MHz	5		MHz

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