2SC1318A

Silicon NPN epitaxial planar type

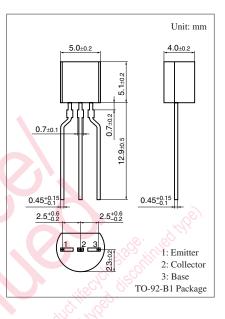
For low-frequency driver amplification Complementary to 2SA0720A

Features

- \bullet High collector-emitter voltage (Base open) $V_{\mbox{CEO}}$
- Optimum for the driver stage of a low-frequency and 25 W to 30 W output amplifier

Symbol	Rating	Unit				
V _{CBO}	80	V				
V _{CEO}	70	v				
V _{EBO}	5	V				
I _C	0.5	А				
I _{CP}	1	Α				
P _C	750	mW				
Tj	150	°C				
T _{stg}	-55 to +150	°C				
	Symbol V _{CBO} V _{CEO} V _{EBO} I _C P _C T _j	Symbol Rating V _{CBO} 80 V _{CEO} 70 V _{EBO} 5 I _C 0.5 I _{CP} 1 P _C 750 T _j 150				





Electrical Characteristics $T_a = 25^{\circ}C \pm 3^{\circ}C$

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-base voltage (Emitter open)	V _{CBO}	$I_{\rm C} = 10 \ \mu A, \ I_{\rm E} = 0$	80			V
Collector-emitter voltage (Base open)	V _{CEO}	$I_{\rm C} = 2 {\rm mA}, I_{\rm B} = 0$	70			V
Emitter-base voltage (Collector open)	V _{EBO}	$I_{\rm E} = 10 \ \mu A, \ I_{\rm C} = 0$	5			V
Collector-base cutoff current (Emitter open)	I _{CBO}	$V_{CB} = 20 V, I_E = 0$			0.1	μΑ
Forward current transfer ratio *1	h _{FE1} *2	$V_{CE} = 10 \text{ V}, \text{ I}_{C} = 150 \text{ mA}$	85		340	
	h _{FE2}	$V_{CE} = 10 \text{ V}, \text{ I}_{C} = 500 \text{ mA}$	40			—
Collector-emitter saturation voltage	V _{CE(sat)}	$I_{\rm C} = 300 \text{ mA}, I_{\rm B} = 30 \text{ mA}$		0.2	0.6	V
Base-emitter saturation voltage	V _{BE(sat)}	$I_{\rm C} = 300 \text{ mA}, I_{\rm B} = 30 \text{ mA}$		0.85	1.50	V
Transition frequency	f _T	$V_{CB} = 10 \text{ V}, I_E = -50 \text{ mA}, f = 200 \text{ MHz}$		120		MHz
Collector output capacitance	C _{ob}	$V_{CB} = 10 \text{ V}, I_E = 0, f = 1 \text{ MHz}$		11	20	pF
(Common base, input open circuited)						

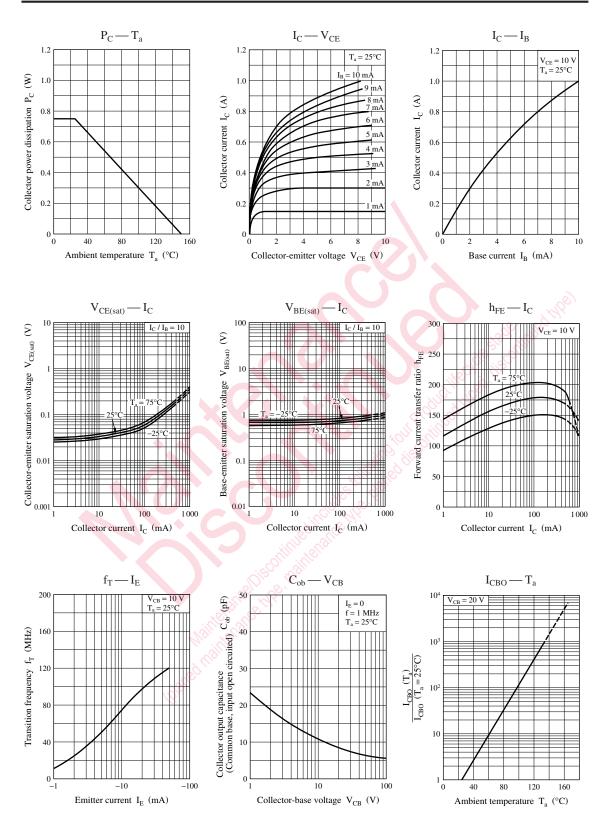
Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

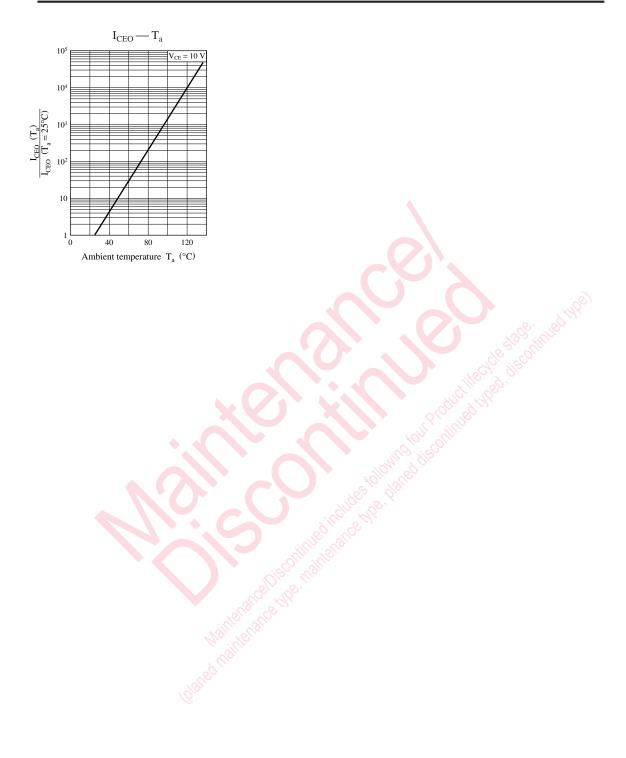
2. *1: Pulse measurement

*2: Rank classification

Rank	Q	R	S
h _{FE1}	85 to 170	120 to 240	170 to 340

Panasonic





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