

isc Silicon NPN Power Transistor
2SC1402
DESCRIPTION

- With TO-3 Package
- High voltage
- Wide area of safe operation
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

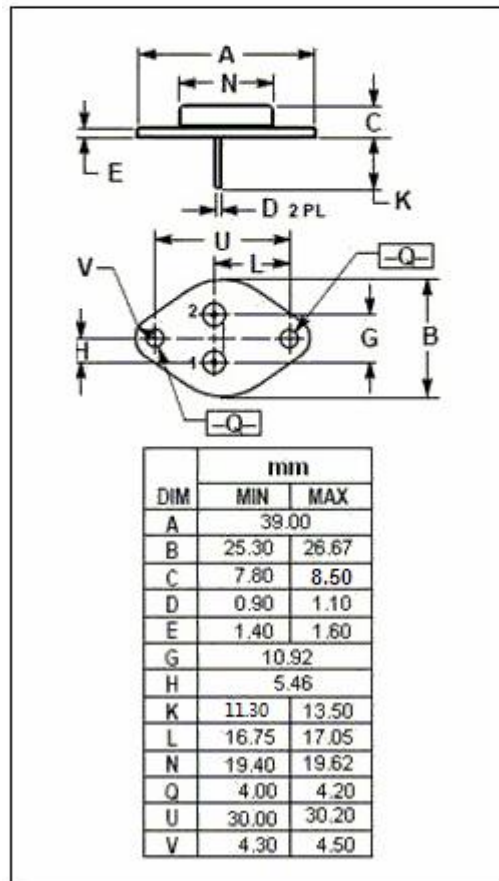
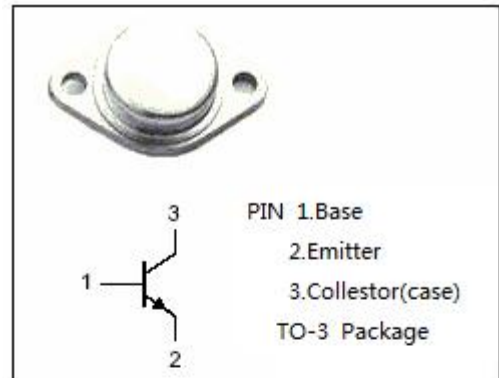
- Power amplifier applications

ABSOLUTE MAXIMUM RATINGS(T_a=25°C)

SYMBOL	PARAMETER	VALUE	UNIT
V _{CBO}	Collector-Base Voltage	140	V
V _{CEO}	Collector-Emitter Voltage	80	V
V _{EBO}	Emitter-Base Voltage	5	V
I _C	Collector Current-Continuous	8	A
P _C	Collector Power Dissipation	70	W
T _J	Junction Temperature	-65~200	°C
T _{stg}	Storage Temperature Range	-65~200	°C

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
R _{th j-c}	Thermal Resistance, Junction to Case	2.5	°C/W



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ELECTRICAL CHARACTERISTICS

 T_c=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _c =5.0A; I _B = 0.5A			2.0	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _c =5.0A; I _B = 0.5A			2.5	V
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _c = 10mA; I _B = 0	80			V
V _{(BR)EBO}	Emitter-Base Breakdown Voltage	I _E = 1mA; I _C = 0	5			V
h _{FE}	DC Current Gain	I _C =3A; V _{CE} = 4V	30			
I _{CBO}	Collector Cutoff Current	V _{CB} = 140V ; I _E = 0			100	uA
I _{EBO}	Emitter Cutoff Current	V _{EB} =5V; I _C = 0			100	uA

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