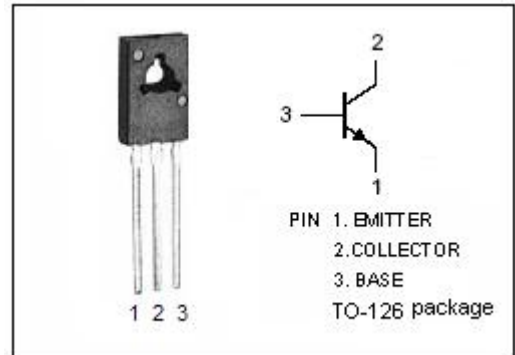


isc Silicon NPN Power Transistor
2SC1567
DESCRIPTION

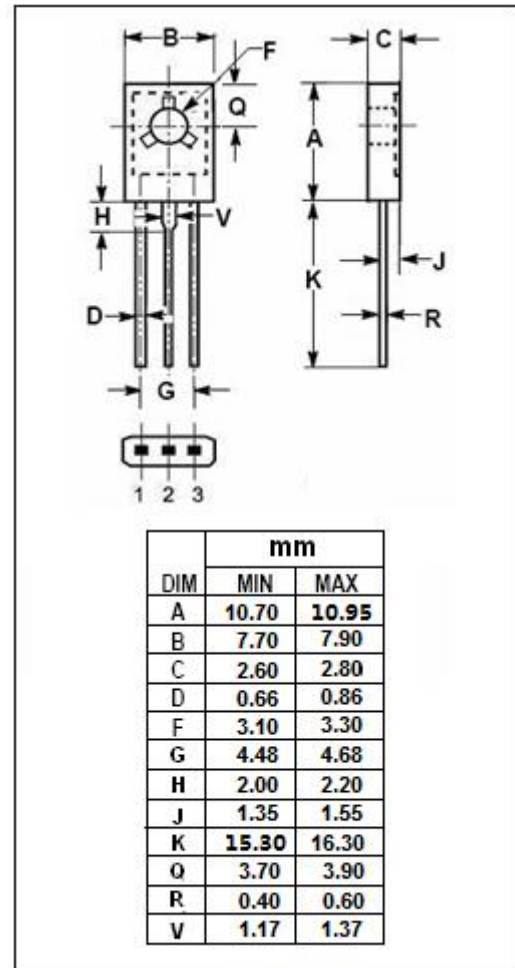
- Collector-Emitter Breakdown Voltage
: $V_{(BR)CEO} = 100V(\text{Min})$
- Good Linearity of h_{FE}
- Complement to Type 2SA794
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Designed for low-frequency high power driver.
- Optimum for the driver stage of low-frequency and 40W to 100W output amplifier.


ABSOLUTE MAXIMUM RATINGS ($T_a = 25^\circ\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	100	V
V_{CEO}	Collector-Emitter Voltage	100	V
V_{EBO}	Emitter-Base Voltage	5	V
I_C	Collector Current-Continuous	0.5	A
I_{CP}	Collector Current-Peak	1	A
P_C	Collector Power Dissipation @ $T_a = 25^\circ\text{C}$	1.2	W
T_J	Junction Temperature	150	$^\circ\text{C}$
T_{stg}	Storage Temperature Range	-40~150	$^\circ\text{C}$



isc Silicon NPN Power Transistor
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ELECTRICAL CHARACTERISTICS
 $T_C=25^{\circ}\text{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
$V_{(BR)CEO}$	Collector-Emitter Breakdown Voltage	$I_C=0.1\text{mA}; I_B=0$	100			V
$V_{(BR)EBO}$	Emitter-Base Breakdown Voltage	$I_E=1\mu\text{A}; I_C=0$	5			V
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	$I_C=500\text{mA}; I_B=50\text{mA}$			0.4	V
$V_{BE(sat)}$	Base-Emitter Saturation Voltage	$I_C=500\text{mA}; I_B=50\text{mA}$			1.2	V
h_{FE-1}	DC Current Gain	$I_C=150\text{mA}; V_{CE}=10\text{V}$	90		330	
h_{FE-2}	DC Current Gain	$I_C=500\text{mA}; V_{CE}=5\text{V}$	50			
f_T	Current-Gain—Bandwidth Product	$I_E=-50\text{mA}; V_{CB}=10\text{V}$		120		MHz
C_{OB}	Output Capacitance	$I_E=0; V_{CB}=10\text{V}, f_{test}=1\text{MHz}$		11		pF

◆ h_{FE1} Classifications

Q	R	S
90-155	130-220	185-330

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