

isc Silicon NPN Power Transistor
2SC1985
DESCRIPTION

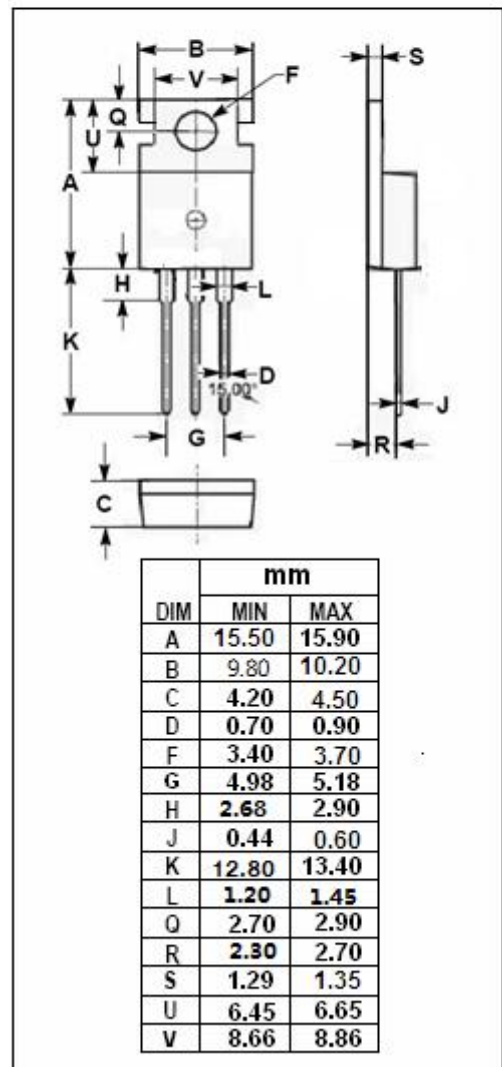
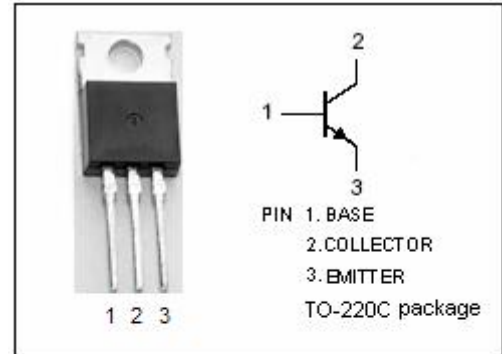
- Silicon NPN tripe diffused mesa
- Collector-Emitter Breakdown Voltage-
: $V_{(BR)CEO} = 60(V)(Min.)$
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- General and industrial purpose

ABSOLUTE MAXIMUM RATINGS($T_a=25^{\circ}C$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	80	V
V_{CEO}	Collector-Emitter Voltage	60	V
V_{EBO}	Emitter-Base Voltage	6	V
I_c	Collector Current-Continuous	6	A
P_c	Total Power Dissipation @ $T_c=25^{\circ}C$	40	W
T_J	Junction Temperature	150	$^{\circ}C$
T_{stg}	Storage Temperature Range	-55~150	$^{\circ}C$



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ELECTRICAL CHARACTERISTICS

 T_c=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = 25mA; I _B = 0	60			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = 3A; I _B = 300mA			1.0	V
I _{CBO}	Collector Cutoff Current	V _{CB} = 80V; I _E = 0			1	mA
I _{EBO}	Emitter Cutoff Current	V _{EB} = 6V; I _C = 0			1	mA
h _{FE}	DC Current Gain	I _C = 1A; V _{CE} = 4V	40			
f _T	Current-Gain—Bandwidth Product	I _E = 500mA; V _{CE} = 12V		10		MHz

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