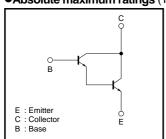
# High-gain Amplifier Transistor (32V, 0.3A) **2SC2062S**

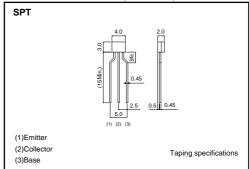
#### Features

- 1) Darlington connection for a high h<sub>FE</sub>. (DC current gain = 5000 (Min.) at VcE = 3V, Ic = 0.1A.)
- 2) High input impedance.

# ● Absolute maximum ratings (Ta=25°C)



# ●External dimensions (Unit : mm)



## ● Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit					
Collector-base voltage	Vcво	40	V					
Collector-emitter voltage	Vces	32	V					
Emitter-base voltage	VEBO	12	V					
Collector current	lc	0.3	A					
Collector power dissipation	Pc	0.3	W					
Junction temperature	Tj	150	°C					
Storage temperature	Tstg	-55 to +150	°C					

# ●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions			
Collector-base breakdown voltage	ВУсво	40	-	-	V	Ic=100μA			
Collector-emitter breakdown voltage	BVces	32	-	-	V	Ic=10mA			
Emitter-base breakdown voltage	ВVево	12	-	-	V	Ιε=100μΑ			
Collector cutoff current	Ісво	-	-	0.1	μА	Vcb=30V			
Emitter cutoff current	Ієво	-	-	0.1	μА	V <sub>EB</sub> =12V			
DC current transfer ratio	hfe	10000	-	_	_	Vce/lc=3V/0.1A			
Collector-emitter saturation voltage	VcE(sat)	-	-	1.4	V	Ic/I <sub>B</sub> =200mA/0.2mA			
Transition frequency	f⊤	-	200	_	MHz	Vce=5V , Ie= -10mA , f=100MHz	*		
Output capacitance	Cob	-	2.5	-	pF	Vcb=10V , IE=0A , f=1MHz			

st Transition frequency of the device.

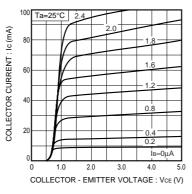
# ●Packaging specifications and hFE

Туре	2SC2062S
Package	SPT
hfe	С
Code	TP
Basic ordering unit (pieces)	5000



# **Transistors**

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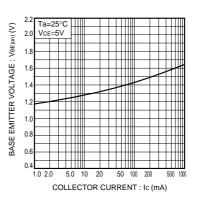
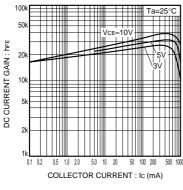
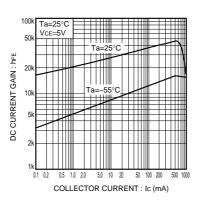


Fig.1 Typical output characteristics (I)

Fig.2 Typical output characteristics ( II )

Fig.3 Base emitter 'ON' voltage vs. collector current





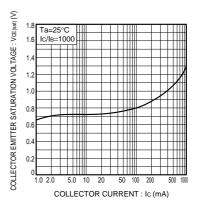
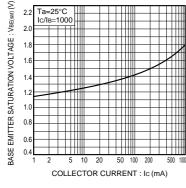
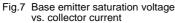


Fig.4 DC current gain vs. collector current (I) Fig.5 DC current gain vs. collector current (II)

Fig.6 Collector emitter saturation voltage vs. collector current





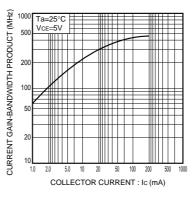


Fig.8 Current gain-bandwidth product vs. collector current

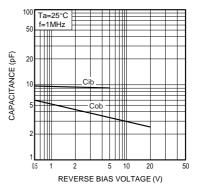


Fig.9 Capacitance vs. reverse bias voltage

# **Appendix**

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