

Silicon NPN Power Transistors

2SC2794

DESCRIPTION

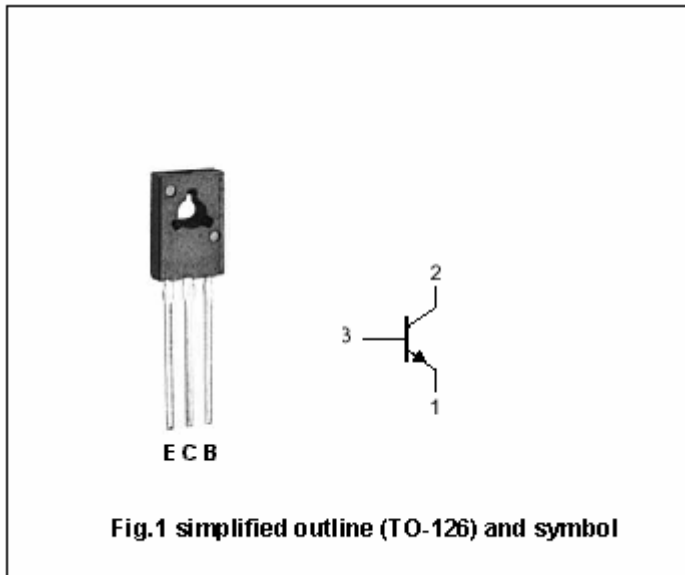
- With TO-126 package
- Low collector saturation voltage

APPLICATIONS

- For medium power linear and switching applications

PINNING

PIN	DESCRIPTION
1	Emitter
2	Collector;connected to mounting base
3	Base



Absolute maximum ratings (Ta=25°C)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V <sub>CBO</sub>	Collector-base voltage	Open emitter	60	V
V <sub>CEO</sub>	Collector-emitter voltage	Open base	60	V
V <sub>EBO</sub>	Emitter -base voltage	Open collector	5	V
I <sub>C</sub>	Collector current (DC)		2	A
I <sub>CM</sub>	Collector current-Peak		6	A
P <sub>C</sub>	Collector power dissipation	T <sub>C</sub> =25°C	25	W
T <sub>j</sub>	Junction temperature		150	°C
T <sub>stg</sub>	Storage temperature		-65~150	°C

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## CHARACTERISTICS

T<sub>j</sub>=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V <sub>(BR)CEO</sub>	Collector-emitter breakdown voltage	I <sub>C</sub> =10mA; I <sub>B</sub> =0	60			V
V <sub>CEsat</sub>	Collector-emitter saturation voltage	I <sub>C</sub> =1A; I <sub>B</sub> =0.1A			0.6	V
V <sub>BE</sub>	Base-emitter on voltage	I <sub>C</sub> =1A; V <sub>CE</sub> =2V			1.3	V
I <sub>CBO</sub>	Collector cut-off current	V <sub>CB</sub> =60V; I <sub>E</sub> =0			100	μA
I <sub>EBO</sub>	Emitter cut-off current	V <sub>EB</sub> =5V; I <sub>C</sub> =0			1	mA
h <sub>FE-1</sub>	DC current gain	I <sub>C</sub> =150mA; V <sub>CE</sub> =2V	50			
h <sub>FE-2</sub>	DC current gain	I <sub>C</sub> =1A; V <sub>CE</sub> =2V	25			
f <sub>T</sub>	Transition frequency	I <sub>C</sub> =100mA; V <sub>CE</sub> =5V		40		MHz

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PACKAGE OUTLINE

