



ISC Silicon NPN Power Transistor

DESCRIPTION

- · Low Collector Saturation Voltage
- · Good Linearity of hFE
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

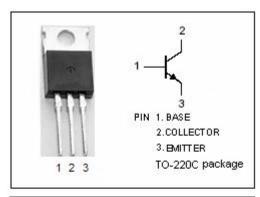


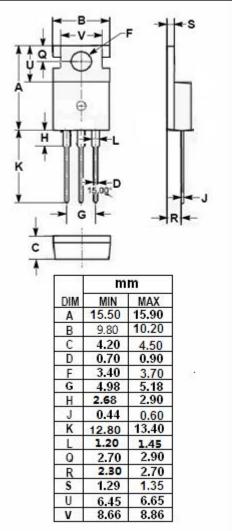
APPLICATIONS

· Designed for switching regulators applications



SYMBOL	PARAMETER	VALUE	UNIT	
Vсво	Collector-Base Voltage	800	V	
V _{CEO}	Collector-Emitter Voltage	500	V	
V _{EBO}	Emitter-Base Voltage	8	V	
lc	Collector Current-Continuous	1.5	Α	
I _{CM}	Collector Current-Pulse	3	А	
Pc	Collector Power Dissipation @ T _C =25 °C	25	W	
TJ	Junction Temperature	150	$^{\circ}$	
T _{stg}	Storage Temperature Range	-55~150	${\mathbb C}$	







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2SC2831

ELECTRICAL CHARACTERISTICS

T_c=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = 30mA ; I _B = 0	500			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = 1A; I _B = 0.2A			1.0	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = 1A; I _B = 0.2A			1.5	V
I _{CBO}	Collector Cutoff Current	V _{CB} =800V; I _E = 0			100	μА
I _{EBO}	Emitter Cutoff Current	V _{EB} = 8V; I _C = 0			100	μА
h _{FE-1}	DC Current Gain	I _C = 0.1A; V _{CE} = 5V	15			
h _{FE-2}	DC Current Gain	I _C = 1A ; V _{CE} = 5V	8			
f _T	Current-Gain—Bandwidth Product	I _E = 0.2A; V _{CE} = 10V		2.5		MHz

NOTICE:

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