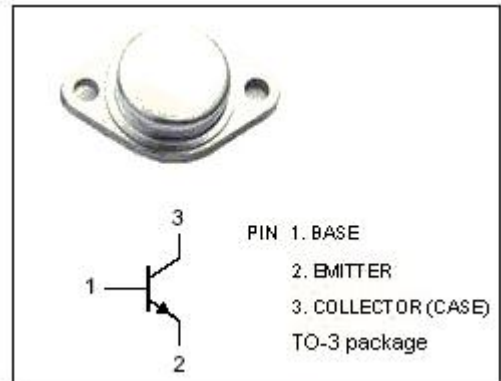


isc Silicon NPN Power Transistor
2SC3026
DESCRIPTION

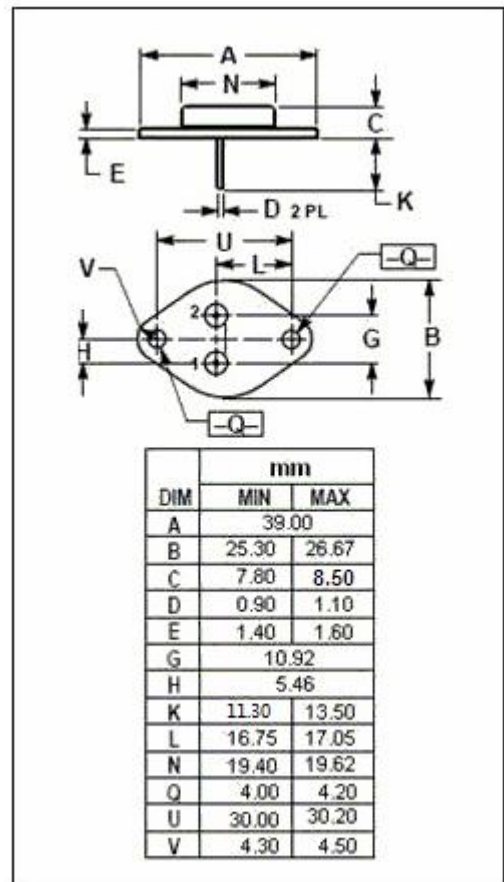
- Collector-Emitter Breakdown Voltage-
: $V_{(BR)CEO} = 800V(\text{Min})$
- Collector-Emitter Saturation Voltage-
: $V_{CE(sat)} = 2.0V(\text{Max}) @ I_C = 5A$
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Designed for high-voltage power switching character display horizontal deflection output applications.


ABSOLUTE MAXIMUM RATINGS ($T_a = 25^\circ\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	1700	V
V_{CEO}	Collector-Emitter Voltage	800	V
V_{EBO}	Emitter-Base Voltage	6	V
I_C	Collector Current-Continuous	5	A
I_{CM}	Collector Current-Peak	6	A
$I_{C(surge)}$	Collector Current-Surge	16	A
P_C	Total Power Dissipation @ $T_C = 25^\circ\text{C}$	50	W
T_J	Junction Temperature	150	$^\circ\text{C}$
T_{stg}	Storage Temperature Range	-45~150	$^\circ\text{C}$



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ELECTRICAL CHARACTERISTICS
T_c=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = 10mA; R _{BE} = ∞	800			V
V _{(BR)EBO}	Emitter-Base Breakdown Voltage	I _E = 10mA; I _C = 0	6			V
I _{CES}	Collector Cutoff Current	V _{CE} = 1700V; R _{BE} = 0			0.5	mA
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = 5A; I _B = 1.25A			2.0	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = 5A; I _B = 1.25A			1.5	V

Switching times

t _{stg}	Storage Time	I _C = 5A; I _{B1} = 1A; I _{B2} = -2.5A		4		μ s
t _f	Fall Time				0.5	μ s

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