

**isc Silicon NPN Power Transistor**
**2SC3632**
**DESCRIPTION**

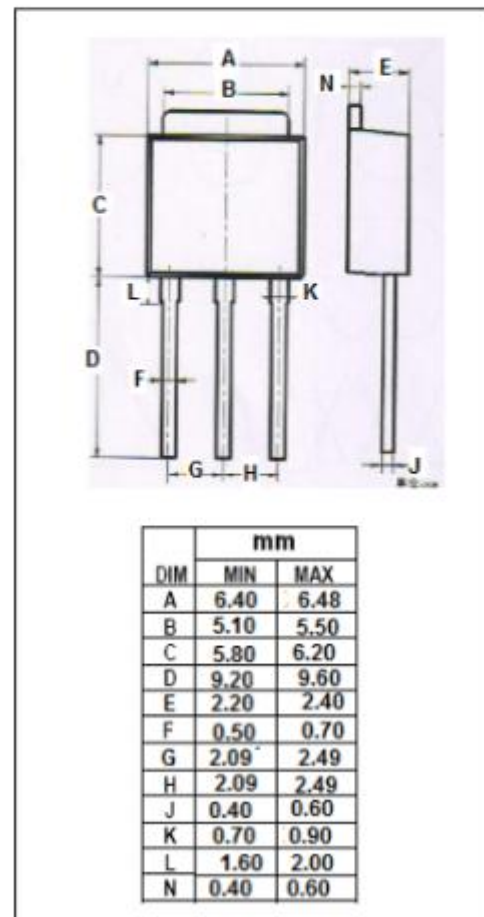
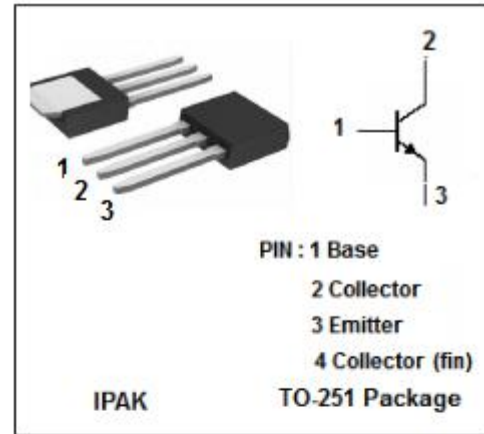
- High Collector-Emitter Voltage
- Low collector saturation voltage
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

**APPLICATIONS**

- High voltage switching.

**ABSOLUTE MAXIMUM RATINGS( $T_a=25^{\circ}\text{C}$ )**

SYMBOL	PARAMETER	VALUE	UNIT
$V_{CBO}$	Collector-Base Voltage	600	V
$V_{CEO}$	Collector-Emitter Voltage	600	V
$V_{EBO}$	Emitter-Base Voltage	7	V
$I_C$	Collector Current-Continuous	1	A
$P_C$	Collector Power Dissipation	2.0	W
$T_J$	Junction Temperature	150	$^{\circ}\text{C}$
$T_{stg}$	Storage Temperature Range	-55~150	$^{\circ}\text{C}$



## isc Silicon NPN Power Transistor

## 2SC3632

## ELECTRICAL CHARACTERISTICS

T<sub>c</sub>=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V <sub>CE(sat)</sub> <sup>NOTE</sup>	Collector-Emitter Saturation Voltage	I <sub>C</sub> = 400mA; I <sub>B</sub> = 80mA			1	V
V <sub>BE(sat)</sub> <sup>NOTE</sup>	Base-Emitter Saturation Voltage	I <sub>C</sub> = 400mA; I <sub>B</sub> = 80mA			1.2	V
I <sub>CBO</sub>	Collector Cutoff Current	V <sub>CB</sub> = 600V; I <sub>E</sub> = 0			10	μ A
I <sub>EBO</sub>	Emitter Cutoff Current	V <sub>EB</sub> = 7V; I <sub>C</sub> = 0			10	μ A
h <sub>FE-1</sub> <sup>NOTE</sup>	DC Current Gain	I <sub>C</sub> = 100mA; V <sub>CE</sub> = 5V	30		120	
h <sub>FE-2</sub> <sup>NOTE</sup>	DC Current Gain	I <sub>C</sub> = 500mA; V <sub>CE</sub> = 2V	5			

NOTE:Pulse test PW≤350us,duty cycle ≤2%

◆ h<sub>FE-1</sub> Classifications

M	L	K
30-60	40-80	60-120

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