

isc Silicon NPN Power Transistor

2SC3642

DESCRIPTION

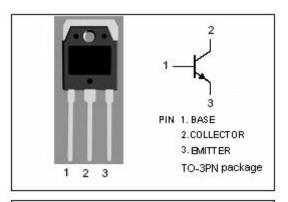
- Low Collector Saturation Voltage
- · High breakdown voltage and high reliability
- Fast switching speed
- Wide ASO
- NPN triple diffused planar silicon transistor
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

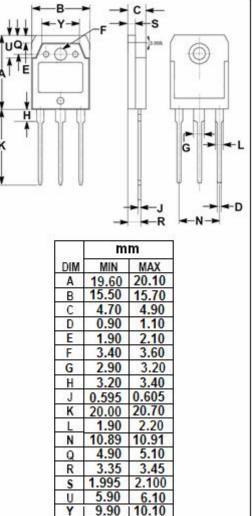
APPLICATIONS

- Ultrahigh deflection display
- Horizontal deflection output application

ABSOLUTE MAXIMUM RATINGS(Ta=25°C)

SYMBOL	PARAMETER	VALUE	UNIT	
V _{CBO}	Collector-Base Voltage	1200	V	
V _{CEO}	Collector-Emitter Voltage	800	V	
V_{EBO}	Emitter-Base Voltage	7	V	
lc	Collector Current-Continuous	6	A	
I _{CM}	Collector Current-Pulse	12	А	
I _B	Base Current-Continuous	2	А	
Pc	Collector Power Dissipation @ T_c =25 °C	100	w	
TJ	Junction Temperature	150	°C	
T _{stg}	Storage Temperature Range	-55~150	°C	





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ELECTRICAL CHARACTERISTICS

T_c=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	МАХ	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = 100mA ; I _B = 0	800			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = 4A; I _B = 0.8A			5.0	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = 4A; I _B = 0.8A			1.5	V
I _{CBO}	Collector Cutoff Current	V _{CB} = 1000V; I _E = 0			10	μA
I _{EBO}	Emitter Cutoff Current	V _{EB} = 7V; I _C = 0			10	μA
h _{FE-}	DC Current Gain	Ic= 0.8A ; Vce= 5V	8			

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