NPN Triple Diffused Planar Silicon Transistor

2SC3687



Ultrahigh-Definition CRT Display Horizontal Deflection Output Applications

Applications

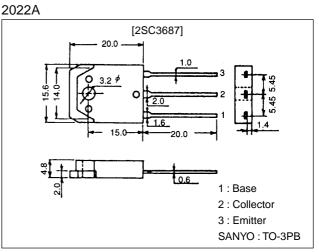
· Ultrahigh-definition color display horizontal deflection output.

Features

- · Fast speed (t_f typ=100ns).
- · High breakdown voltage (V_{CBO} =1500V).
- High reliability (adoption of HVP process).
- · Adoption of MBIT process.

Package Dimensions

unit:mm



Specifications

Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	V _{CBO}		1500	V
Collector-to-Emitter Voltage	VCEO		800	V
Emitter-to-Base Voltage	VEBO		6	V
Collector Current	ι _C		8	A
Collector Current (Pulse)	I _{CP}		25	A
Collector Dissipation	PC	Tc=25°C	150	W
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

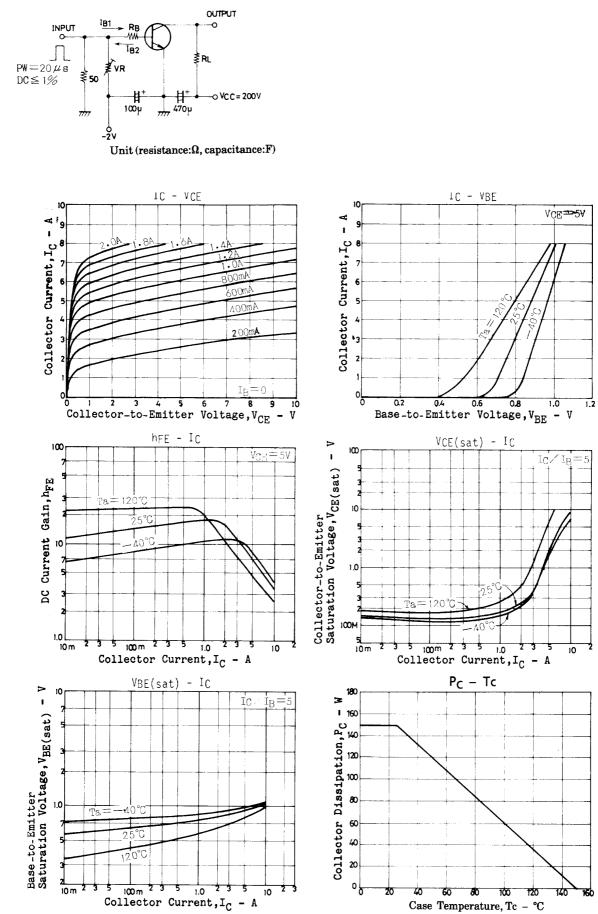
Electrical Characteristics at Ta = 25°C

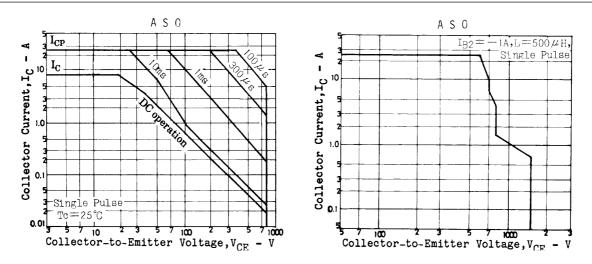
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Office
Collector Cutoff Current	ICES	V _{CE} =1500V, R _{BE} =0			1.0	mA
Collector-to-Emitter Sastain Voltage	V _{CEO(sus)}	I _C =100mA, I _B =0	800			V
Emitter Cutoff Current	IEBO	V _{EB} =4V, I _C =0			1.0	mA
Collector-to-Emitter Saturation Voltage	V _{CE(sat)}	I _C =6A, I _B =1.5A			5	V
Base-to-Emitter Saturation Voltage	V _{BE(sat)}	I _C =6A, I _B =1.5A			1.5	V
DC Current Gain	hFE	V _{CE} =5V, I _C =1.0A	8			
Storage Time	^t stg	I _C =6A, I _{B1} =1.2A, I _{B2} =-2.4A			3.0	μs
Fall Time	t _f	I _C =6A, I _{B1} =1.2A, I _{B2} =-2.4A		0.1	0.2	μs

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SANYO Electric Co., Ltd. Semiconductor Bussiness Headquaters TOKYO OFFICE Tokyo Bldg., 1-10, 1 Chome, Ueno, Taito-ku, TOKYO, 110-8534 JAPAN

Switching Time Test Circuit





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