

isc Silicon NPN Power Transistor

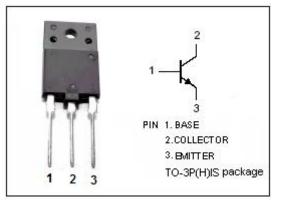
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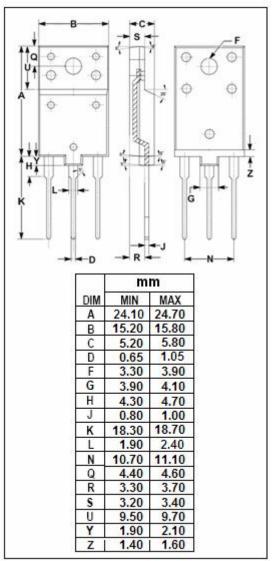
DESCRIPTION

- High Breakdown Voltage-
- : V_{CEO}= 600V (Min)
- High Switching Speed
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Horizontal deflection output for high resolution display.
- · High speed switching regulator output applications.





ABSOLUTE MAXIMUM RATINGS(Ta=25°C)

SYMBOL	PARAMETER	VALUE	UNIT
Vсво	Collector-Base Voltage	1400	V
V _{CEO}	Collector-Emitter Voltage	600	V
V _{EBO}	Emitter-Base Voltage	5	V
lc	Collector Current- Continuous	8	A
Ісм	Collector Current- Peak	15	A
I _B	Base Current- Continuous	4	A
Pc	Collector Power Dissipation @ T _c =25°C	50	W
TJ	Junction Temperature	150	°C
T _{stg}	Storage Temperature Range	-55~150	°C

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ELECTRICAL CHARACTERISTICS

Tc=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = 5mA; I _B = 0	600			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = 6A; I _B = 1.5A			5.0	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = 6A; I _B = 1.5A			1.5	V
Ісво	Collector Cutoff Current	V _{CB} = 1400V; I _E = 0			1.0	mA
I _{EBO}	Emitter Cutoff Current	V _{EB} = 5V; I _C = 0			10	μ Α
h _{FE}	DC Current Gain	I _C = 1A; V _{CE} = 5V	8			
f _T	Current-Gain—Bandwidth Product	I _C = 0.1A; V _{CE} = 10V	3			MHz
Сов	Output Capacitance	I _E = 0; V _{CB} = 10V; f _{test} = 1.0MHz		150		pF

Switching Times , Resistive load

t _{stg}	Storage Time			4	μ S
t _f	Fall Time	I _C = 6Α, I _B =1.2Α;		0.5	μ S

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