Transistors Panasonic

2SC3929

Silicon NPN epitaxial planar type

For low-frequency output amplification Complementary to 2SA1531

■ Features

- Low noise voltage NV
- \bullet High forward current transfer ratio h_{FE}
- S-Mini type package, allowing downsizing of the equipment and automatic insertion through the tape packing.

■ Absolute Maximum Ratings $T_a = 25$ °C

Parameter	Symbol	Rating	Unit	
Collector-base voltage (Emitter open)	V _{CBO}	35	V	
Collector-emitter voltage (Base open)	V _{CEO}	35	V	
Emitter-base voltage (Collector open)	V _{EBO}	5	V	
Collector current	I_{C}	50	mA	
Peak collector current	I _{CP}	I _{CP} 100		
Collector power dissipation	P _C	150	mW	
Junction temperature	T_{j}	150	°C	
Storage temperature	T _{stg}	-55 to +150	°C	

■ Package

- Code
 - SMini3-G1
- Pin Name
 - 1. Base
 - 2. Emitter3. Collector

■ Marking Symbol: S

■ Electrical Characteristics $T_a = 25$ °C±3°C

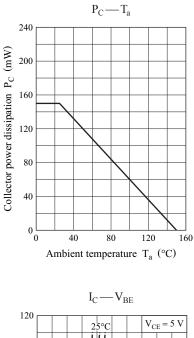
Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-base voltage (Emitter open)	V _{CBO}	$I_C = 10 \mu\text{A}, I_E = 0$	35			V
Collector-emitter voltage (Base open)	V _{CEO}	$I_{\rm C} = 2 \text{ mA}, I_{\rm B} = 0$	35			V
Emitter-base voltage (Collector open)	V_{EBO}	$I_E = 10 \mu A, I_C = 0$	5			V
Base-emitter voltage	V_{BE}	$V_{CE} = 1 \text{ V, } I_{C} = 100 \text{ mA}$		0.7	1.0	V
Collector-base cutoff current (Emitter open)	I _{CBO}	$V_{CB} = 10 \text{ V}, I_{E} = 0$			0.1	μΑ
Collector-emitter cutoff current (Base open)	I _{CEO}	$V_{CE} = 10 \text{ V}, I_{B} = 0$			1	μΑ
Forward current transfer ratio *	h_{FE}	$V_{CE} = 5 \text{ V}, I_{C} = 2 \text{ mA}$	180		700	_
Collector-emitter saturation voltage	V _{CE(sat)}	$I_C = 100 \text{ mA}, I_B = 10 \text{ mA}$			0.6	V
Transition frequency	f_T	$V_{CB} = 5 \text{ V}, I_{E} = -2 \text{ mA}, f = 200 \text{ MHz}$		100		MHz
Noise voltage	NV	$V_{CB} = 10 \text{ V}, I_C = 1 \text{ mA}, G_V = 80 \text{ dB},$ $R_g = 100 \text{ k}\Omega, \text{Function} = \text{FLAT}$			150	mV

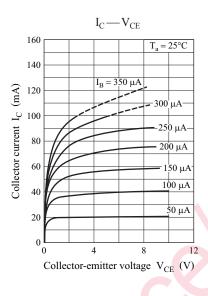
Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

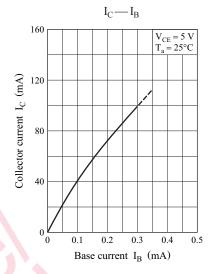
2. *: Rank classification

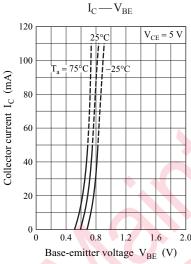
Rank	R	S	Т
$h_{ m FE}$	180 to 360	260 to 520	360 to 700
Merking symbol	SR	SS	ST

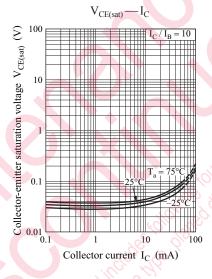
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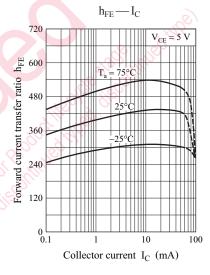


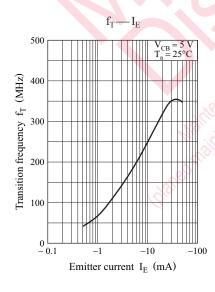


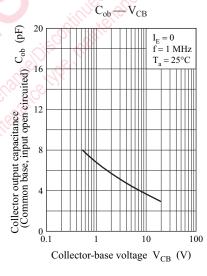


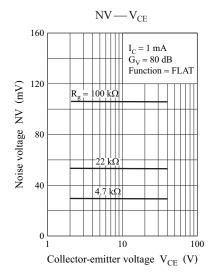




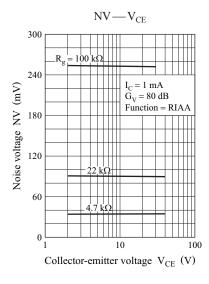


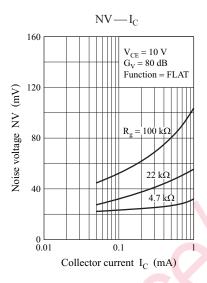


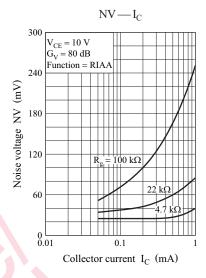


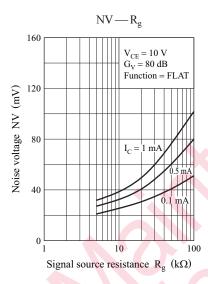


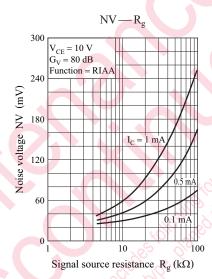
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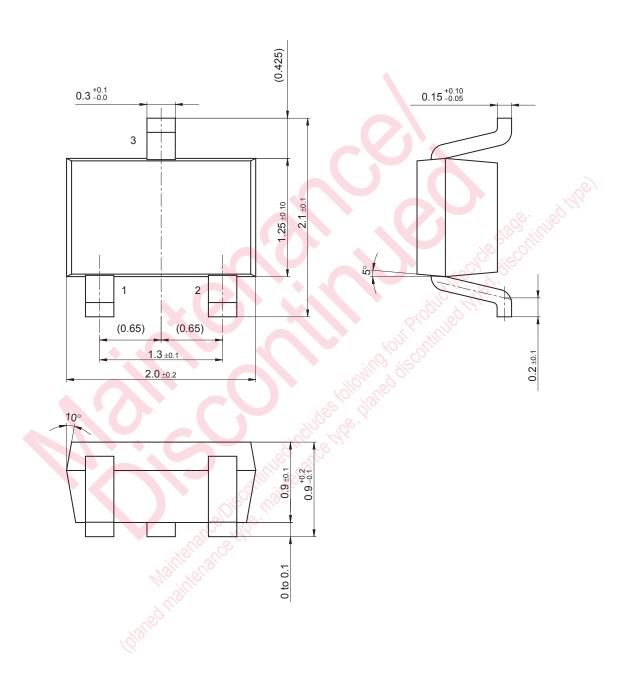




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SMini3-G1 Unit: mm



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