

isc Silicon NPN Power Transistor
2SC4024
DESCRIPTION

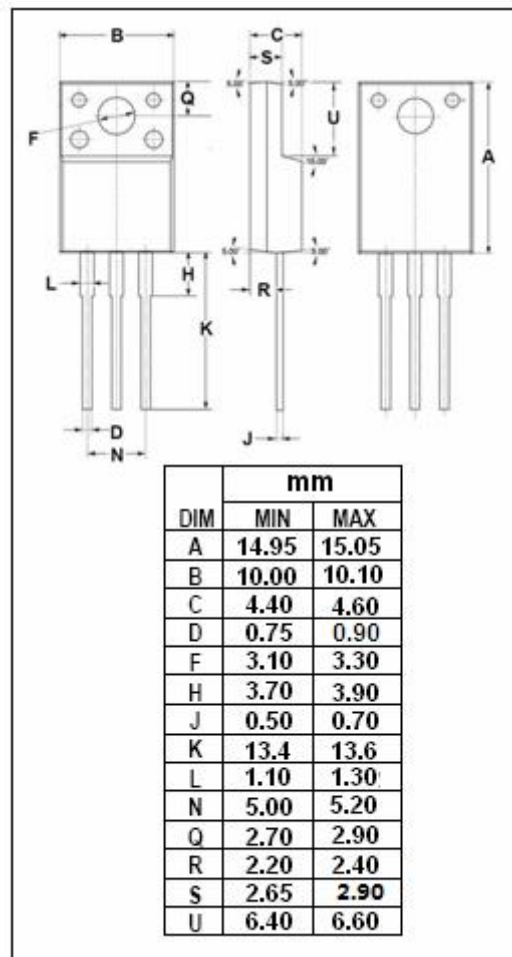
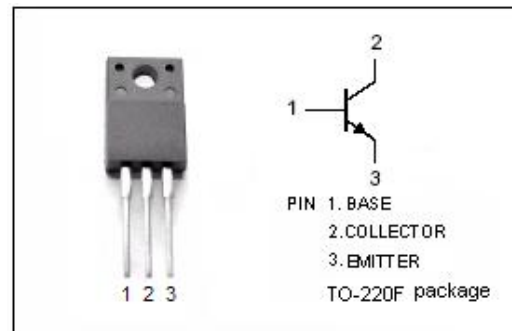
- Collector-Emitter Breakdown Voltage-
: $V_{(BR)CEO} = 50V(\text{Min})$
- High DC Current Gain
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- DC-DC converter, Emergency lighting Inverter and general purpose

ABSOLUTE MAXIMUM RATINGS ($T_a = 25^\circ\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	100	V
V_{CEO}	Collector-Emitter Voltage	50	V
V_{EBO}	Emitter-Base Voltage	15	V
I_C	Collector Current-Continuous	10	A
I_B	Base Current-Continuous	3	A
P_C	Collector Power Dissipation @ $T_C = 25^\circ\text{C}$	35	W
T_J	Junction Temperature	150	$^\circ\text{C}$
T_{stg}	Storage Temperature	-55~150	$^\circ\text{C}$



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ELECTRICAL CHARACTERISTICS

T_j=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = 25mA; I _B = 0	50			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = 5A; I _B = 0.1A			0.5	V
I _{CBO}	Collector Cutoff Current	V _{CB} = 100V; I _E = 0			100	μ A
I _{EBO}	Emitter Cutoff Current	V _{EB} = 15V; I _C = 0			100	μ A
h _{FE}	DC Current Gain	I _C = 1A; V _{CE} = 4V	300		1600	
C _{OB}	Output Capacitance	I _E = 0; V _{CB} = 10V; f= 1MHz		150		pF
f _T	Current-Gain—Bandwidth Product	I _E = -0.5A; V _{CE} = 12V		24		MHz

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