2SC4081RT1

General Purpose Amplifier Transistor

NPN Surface Mount

Features

- Moisture Sensitivity Level: 1
- Pb-Free Package is Available

MAXIMUM RATINGS $(T_A = 25^{\circ}C)$

Rating	Symbol	Value	Unit
Collector-Base Voltage	V _{(BR)CBO}	60	Vdc
Collector-Emitter Voltage	V _{(BR)CEO}	50	Vdc
Emitter-Base Voltage	V _{(BR)EBO}	7.0	Vdc
Collector Current – Continuous	I _C	100	mAdc
Collector Current – Peak	I _{C(P)}	200	mAdc

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Power Dissipation	P _D	200	mW
Junction Temperature	TJ	150	°C
Storage Temperature	T _{stg}	-55 to +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

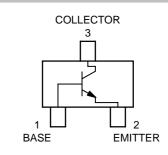
Characteristic	Symbol	Min	Max	Unit
Collector–Emitter Breakdown Voltage (I _C = 2.0 mAdc, I _B = 0)	V _{(BR)CEO}	50	-	Vdc
Collector–Base Breakdown Voltage $(I_C = 10 \mu Adc, I_E = 0)$	V _{(BR)CBO}	60	-	Vdc
Emitter–Base Breakdown Voltage ($I_E = 10 \mu Adc, I_C = 0$)	V _{(BR)EBO}	7.0	-	Vdc
Collector–Base Cutoff Current (V _{CB} = 60 Vdc, I _E = 0)	I _{CBO}	-	0.1	μAdc
	I _{CEO}	1 1 1	0.1 2.0 1.0	μAdc μAdc mAdc
DC Current Gain (Note 1) (V _{CE} = 6.0 Vdc, I _C = 2.0 mAdc)	h _{FE}	180	390	-
Collector–Emitter Saturation Voltage (I _C = 100 mAdc, I _B = 10 mAdc)	V _{CE(sat)}	-	0.5	Vdc

^{1.} Pulse Test: Pulse Width \leq 300 μ s, D.C. \leq 2%.



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SC-70 CASE 419

MARKING DIAGRAM



81 = Device Code
M = Date Code*
= Pb-Free Package

(Note: Microdot may be in either location)
*Date Code orientation may vary depending upon manufacturing location.

Device*	Package	Shipping [†]
2SC4081RT1	SC-70	3000/Tape & Reel
2SC4081RT1G	SC-70 (Pb-Free)	3000/Tape & Reel

ORDERING INFORMATION

*The "T1" suffix refers to a 7 inch reel.

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.







SC-70 (SOT-323) CASE 419 ISSUE R

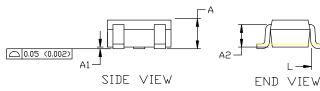
DATE 11 OCT 2022

NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1982.
- 2. CONTROLLING DIMENSION: INCH

	MILLIMETERS				INCHES		
DIM	MIN.	N□M.	MAX.	MIN.	N□M.	MAX.	
Α	0.80	0.90	1.00	0.032	0.035	0.040	
A1	0.00	0.05	0.10	0.000	0.002	0.004	
A2	0.70 REF				0.028 BSC		
b	0.30	0.35	0.40	0.012	0.014	0.016	
С	0.10	0.18	0.25	0.004	0.007	0.010	
D	1.80	2.00	2.20	0.071	0.080	0.087	
E	1.15	1.24	1.35	0.045	0.049	0.053	
е	1.20	1.30	1.40	0.047	0.051	0.055	
e1	0.65 BSC				0.026 BS	C	
L	0.20	0.38	0.56	0.008	0.015	0.022	
HE	2.00	2.10	2.40	0.079	0.083	0.095	

SCALE 4:1



GENERIC MARKING DIAGRAM

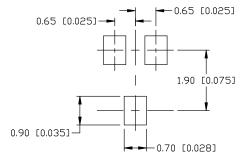


XX = Specific Device Code

M = Date Code

■ = Pb-Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.



For additional information on our Pb-Free strategy and soldering details, please download the IIN Semiconductor Soldering and Mounting Techniques Reference Manual, SDLDERRM/D.

SOLDERING FOOTPRINT

STYLE 1: CANCELLED	STYLE 2: PIN 1. ANODE 2. N.C. 3. CATHODE	STYLE 3: PIN 1. BASE 2. EMITTER 3. COLLECTOR	STYLE 4: PIN 1. CATHODE 2. CATHODE 3. ANODE	STYLE 5: PIN 1. ANODE 2. ANODE 3. CATHODE	
STYLE 6:	STYLE 7:	STYLE 8:	STYLE 9:	STYLE 10:	STYLE 11:
PIN 1. EMITTER	PIN 1. BASE	PIN 1. GATE	PIN 1. ANODE	PIN 1. CATHODE	PIN 1. CATHODE
2. BASE	2. EMITTER	2. SOURCE	2. CATHODE	2. ANODE	CATHODE
COLLECTOR	COLLECTOR	3. DRAIN	CATHODE-ANODE	3. ANODE-CATHODE	CATHODE

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DESCRIPTION:	SC-70 (SOT-323)		PAGE 1 OF 1	

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