

Silicon NPN Power Transistors

2SC4139

DESCRIPTION

- With TO-3PN package
- High voltage
- High speed switching

APPLICATIONS

- For switching regulator and general purpose applications

PINNING

PIN	DESCRIPTION
1	Base
2	Collector;connected to mounting base
3	Emitter

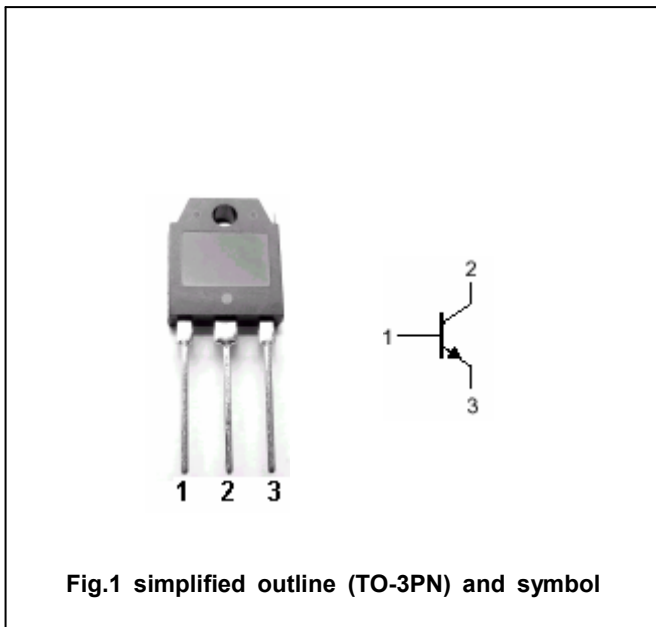


Fig.1 simplified outline (TO-3PN) and symbol

Absolute maximum ratings(Ta=□)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V <sub>CBO</sub>	Collector-base voltage	Open emitter	500	V
V <sub>CEO</sub>	Collector-emitter voltage	Open base	400	V
V <sub>EBO</sub>	Emitter-base voltage	Open collector	10	V
I <sub>C</sub>	Collector current		15	A
I <sub>CP</sub>	Collector current-pulse		30	A
I <sub>B</sub>	Base current		5	A
P <sub>C</sub>	Collector power dissipation	T <sub>C</sub> =25□	120	W
T <sub>j</sub>	Junction temperature		150	□
T <sub>stg</sub>	Storage temperature		-55~150	□

## Silicon NPN Power Transistors

## 2SC4139

## CHARACTERISTICS

T<sub>j</sub>=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V <sub>(BR)CEO</sub>	Collector-emitter breakdown voltage	I <sub>C</sub> =25mA ; I <sub>B</sub> =0	400			V
V <sub>CEsat</sub>	Collector-emitter saturation voltage	I <sub>C</sub> =8A; I <sub>B</sub> =1.6A			0.5	V
V <sub>BEsat</sub>	Base-emitter saturation voltage	I <sub>C</sub> =8A; I <sub>B</sub> =1.6A			1.3	V
I <sub>CBO</sub>	Collector cut-off current	V <sub>CB</sub> =500V ; I <sub>E</sub> =0			100	μA
I <sub>EBO</sub>	Emitter cut-off current	V <sub>EB</sub> =10V; I <sub>C</sub> =0			100	μA
h <sub>FE</sub>	DC current gain	I <sub>C</sub> =8A ; V <sub>CE</sub> =4V	10		30	
f <sub>T</sub>	Transition frequency	I <sub>E</sub> =-1.5A ; V <sub>CE</sub> =12V		10		MHz
C <sub>OB</sub>	Collector output capacitance	f=1MHz ; V <sub>CB</sub> =10V		85		pF

## Switching times

t <sub>on</sub>	Turn-on time	I <sub>C</sub> =8A; I <sub>B1</sub> =0.8A; I <sub>B2</sub> =-1.6A; R <sub>L</sub> =25Ω V <sub>CC</sub> =200V			1.0	μs
t <sub>stg</sub>	Storage time				3.0	μs
t <sub>f</sub>	Fall time				0.5	μs

Silicon NPN Power Transistors

2SC4139

PACKAGE OUTLINE

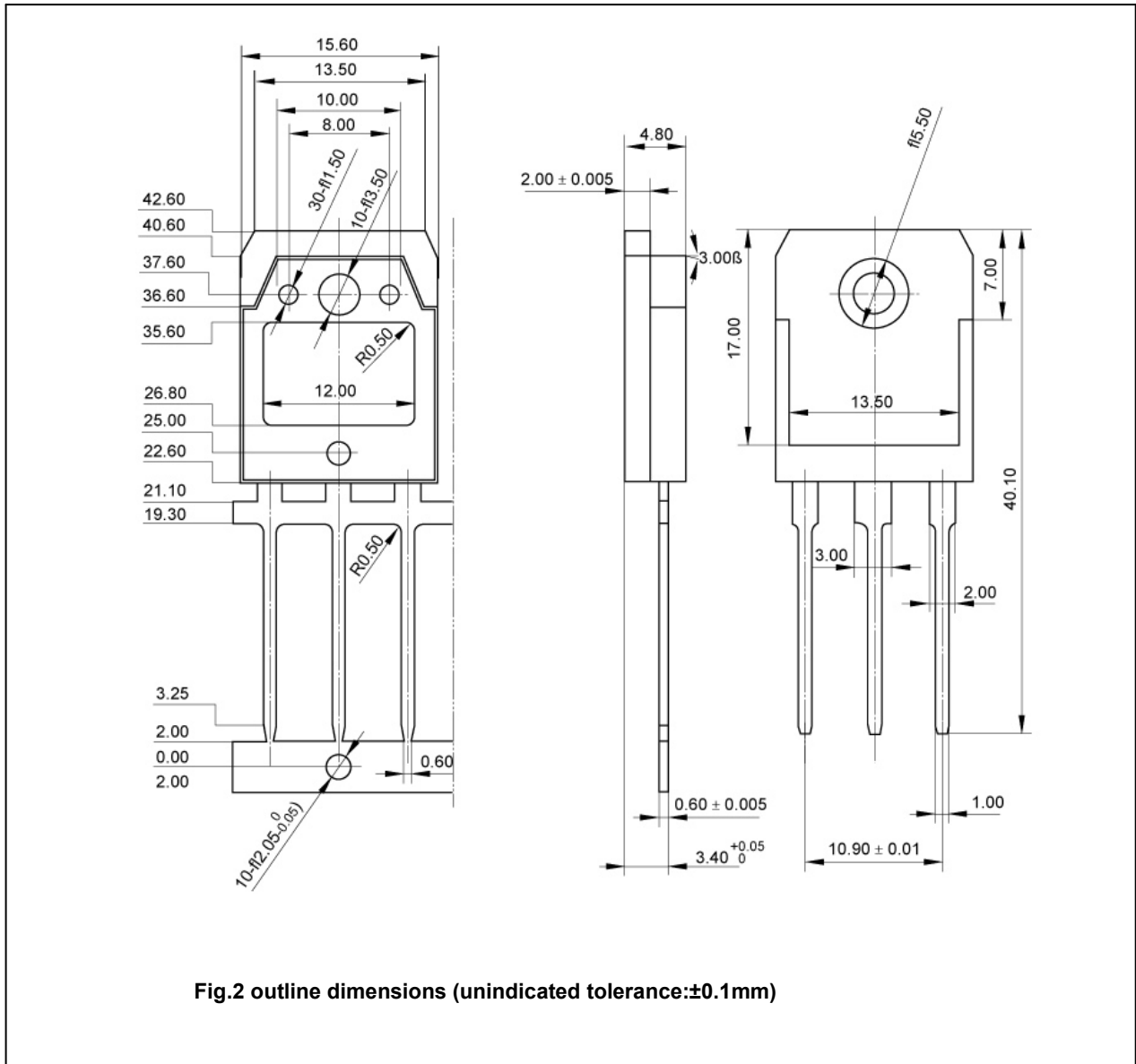


Fig.2 outline dimensions (unindicated tolerance:  $\pm 0.1$  mm)

Silicon NPN Power Transistors

2SC4139

www.DataSheet4U.com

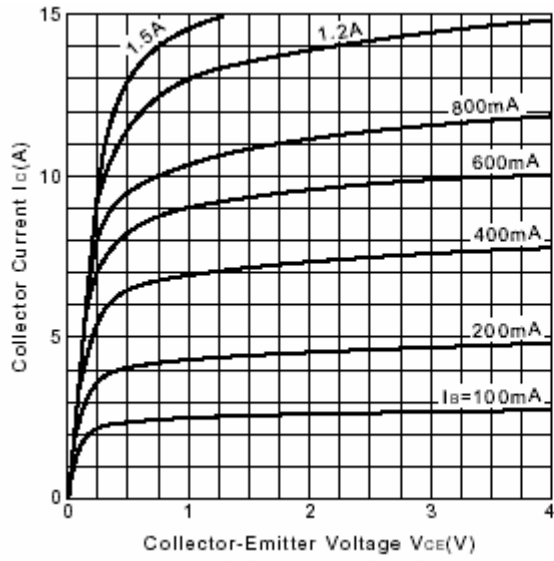


Fig.3 Static Characteristic

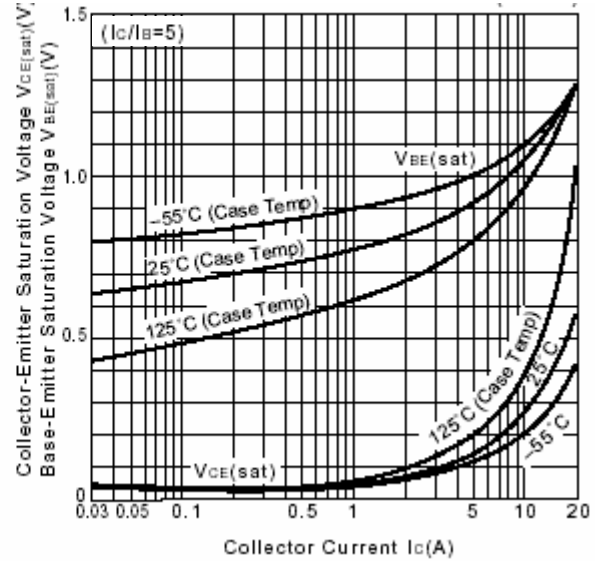


Fig.4 Base-Emitter Saturation Voltage  
Collector-Emitter Saturation Voltage

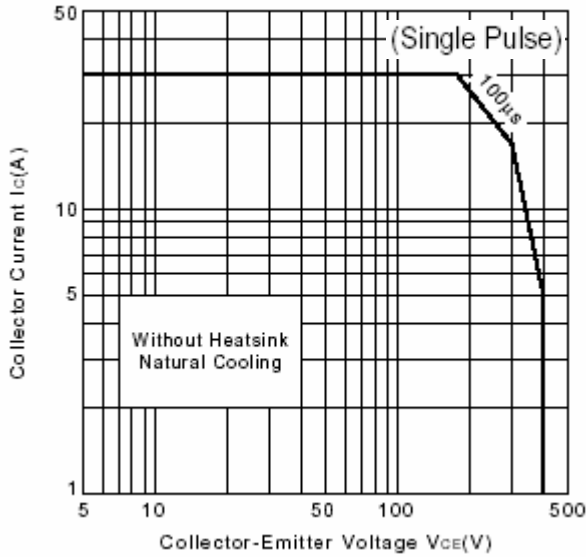


Fig.5 Safe Operating Area

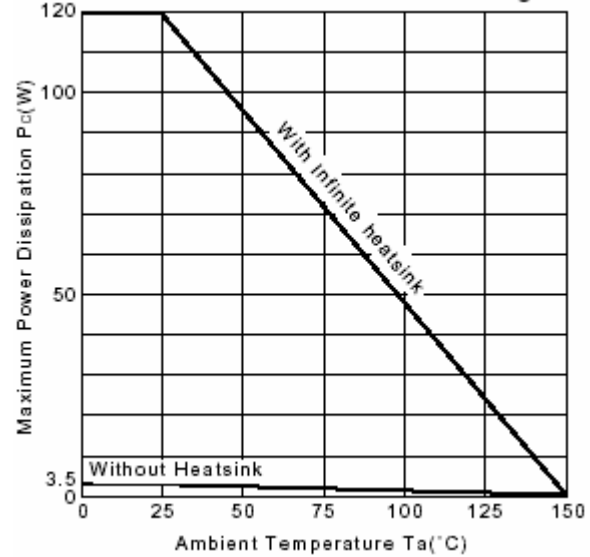


Fig.6 Pc-Ta Derating

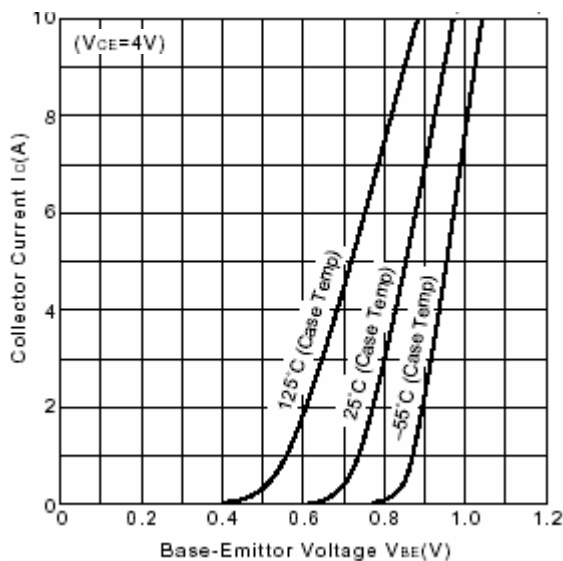


Fig.7 Ic-VBE

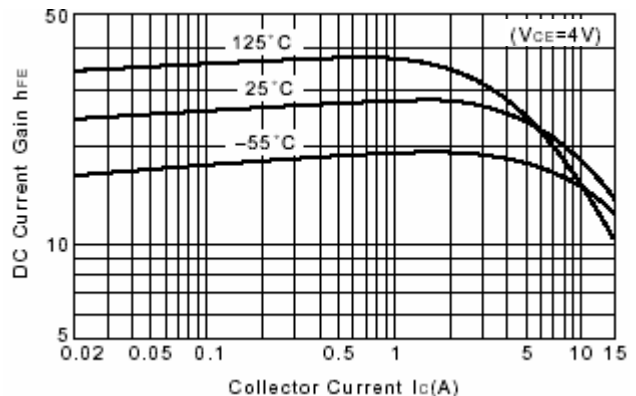


Fig.8 DC current Gain