

isc Silicon NPN Power Transistor

DESCRIPTION

- · Fast switching speed
- NPN epitaxial planar silicon transistor
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

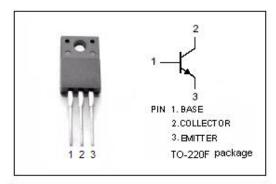
APPLICATIONS

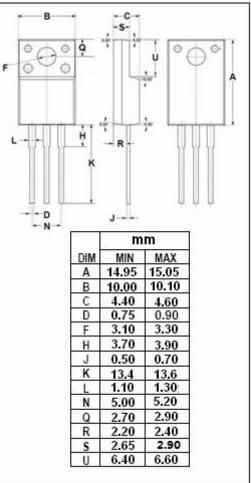


- · High deflection CRT display
- Horizontal deflection output applications

ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER	VALUE	UNIT
V _{CBO}	Collector-Base Voltage	400	V
Vceo	Collector-Emitter Voltage	200	V
V _{EBO}	Emitter-Base Voltage	6	V
l _C	Collector Current-Continuous	7	Α
l _B	Base Current-Continuous	4	А
Pc	Collector Power Dissipation @Tc=25℃	30	W
TJ	Junction Temperature	150	$^{\circ}$
T _{stg}	Storage Temperature	-55~150	







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2SC4478

ELECTRICAL CHARACTERISTICS

Tj=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = 25mA; I _B = 0	200			V
V _{CE} (sat)	Collector-Emitter Saturation Voltage	I _C = 5A; I _B = 0.5A			0.8	V
V _{BE} (sat)	Base-Emitter Saturation Voltage	I _C = 5A; I _B = 0.5A			1.5	V
І _{СВО}	Collector Cutoff Current	V _{CB} = 250V; I _E = 0			100	μА
I _{EBO}	Emitter Cutoff Current	V _{EB} = 5V; I _C = 0			100	μА
h _{FE-1}	DC Current Gain	I _C = 1A; V _{CE} = 1V	15			
h _{FE-2}	DC Current Gain	I _C = 5A; V _{CE} = 1V	10		50	
f⊤	Current-Gain—Bandwidth Product	I _E = -0.5A; V _{CE} = 10V		40		MHz

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