

SANYO Semiconductors DATA SHEET

An ON Semiconductor Company

2SC4482 — NPN Epitaxial Planar Silicon Transistor High-Current Switching Applications

Features

- · Low saturation voltage
- · Large current capacity
- · High-speed switching

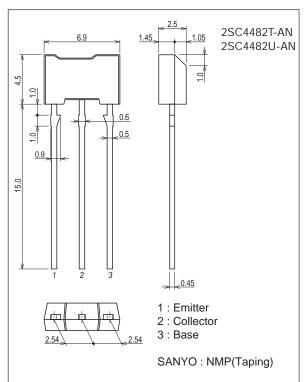
Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	VCBO		60	V
Collector-to-Emitter Voltage	VCEO		20	V
Emitter-to-Base Voltage	VEBO		6	V
Collector Current	IC		5	Α
Collector Current (Pulse)	ICP		8	Α
Collector Dissipation	PC		1	W
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Package Dimensions

unit : mm (typ) 7540-001

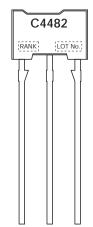


Product & Package Information

Package : NMP(Taping)JEITA, JEDEC : SC-71

• Minimum Packing Quantity: 2,500 pcs./box

Marking(NMP(Taping))



Electrical Connection



SANYO Semiconductor Co., Ltd.

http://www.sanyosemi.com/en/network/

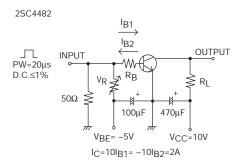
Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
Parameter	Symbol	Conditions	min	typ	max	UIIIL
Collector Cutoff Current	ICBO	V _{CB} =50V, I _E =0A			100	nA
Emitter Cutoff Current	IEBO	VEB=5V, IC=0A			100	nA
DC Current Gain	h _{FE} 1	V _{CE} =2V, I _C =500mA	200*		560*	
DC Current Gain	hFE2	V _{CE} =2V, I _C =3A	95			
Gain-Bandwidth Product	fT	V _{CE} =10V, I _C =50mA		150		MHz
Output Capacitance	Cob	V _{CB} =10V, f=1MHz		45		pF
Collector-to-Emitter Saturation Voltage	V _{CE} (sat)	I _C =3A, I _B =60mA		220	500	mV
Base-to-Emitter Saturation Voltage	V _{BE} (sat)	IC=3A, IB=60mA			1.5	V
Collector-to-Base Breakdown Voltage	V(BR)CBO	I _C =10μA, I _E =0A	60			V
Collector-to-Emitter Breakdown Voltage	V(BR)CEO	IC=1mA, RBE=∞	20			V
Emitter-to-Base Breakdown Voltage	V(BR)EBO	I _E =10μA, I _C =0A	6			V
Turn-ON Time	ton			30		ns
Storage Time	tstg	See specified Test Circuit.		300		ns
Fall Time	tf			40		ns

*: The 2SC4482 is classified by 500mA hFE as follows:

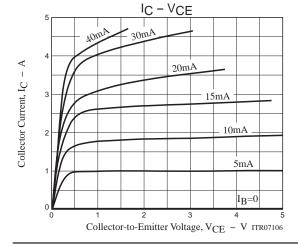
Rank	T	U
h _{FE}	200 to 400	280 to 560

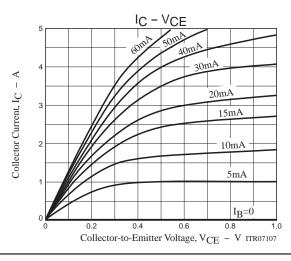
Switching Time Test Circuit

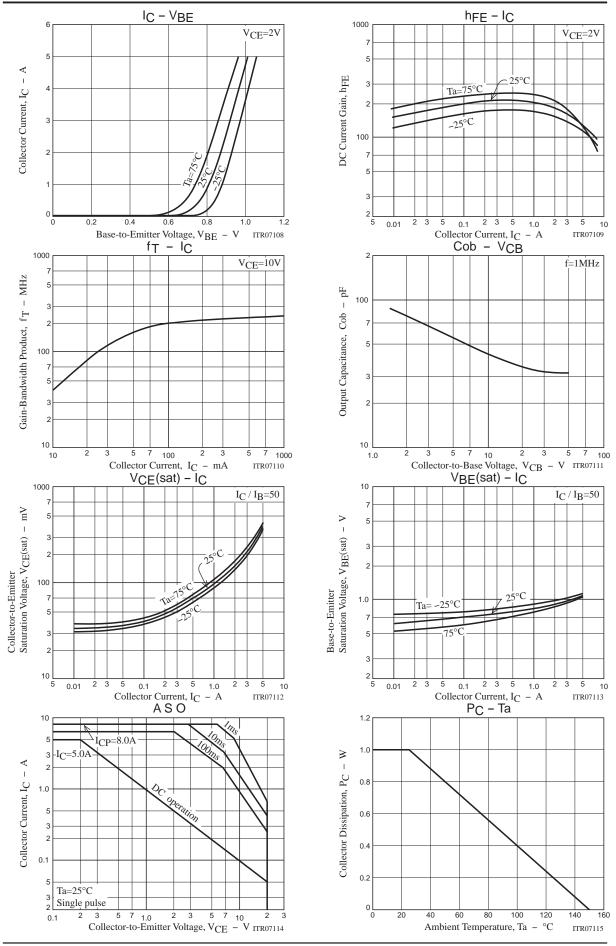


Ordering Information

•			
Device Package		Shipping	memo
2SC4482T-AN	NMP(Taping)	2,500pcs./box	Pb Free
2SC4482U-AN	NMP(Taping)	2,500pcs./box	Pb Fiee







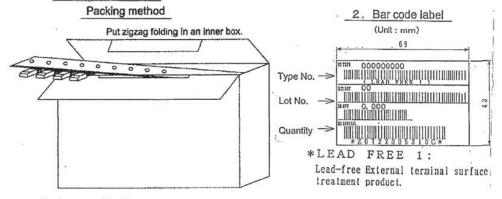
Bag Packing Specification

2SC4482T-AN, 2SC4482U-AN

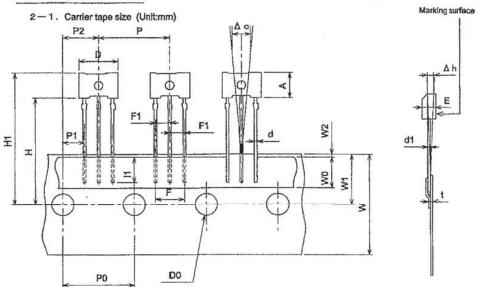
NMP (Zigzag folding)

Storage package	Package	Maximum Number of devices contained (pcs.)		Packing format		
Outline name type	Inner box No.	Storage quantity	Outer box (C-6)	Outer box (C-8)		
NMP	AN/AZ	C-3 Inner box Dimensions :rnm(external) 3 3 0 × 4 5 × 1 2 5	2,500	8 inner boxes contained(20,000pcs.) Outer box Dimensions.mm(external) 5 8 5 × 3 4 5 × 1 9 5	4 inner boxes contained (10,000pcs; Outer box Dimensions:mm(external 3 4 5 × 3 0 0 × 1 9 5	

1. Packing format



2. Taping specifications

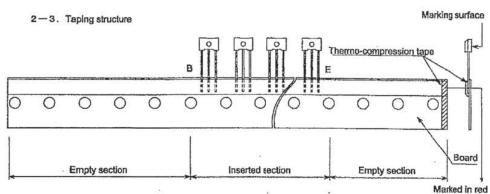


2-2. Taping size standard

Item	Symbol	Standard	Tolerance
	D	6.9	±0.2
Work piece outside diameter	E	2.5	±0.2
Work piece height	Α	4.5	±0.2
Lead wire diameter	d	0.5	±0.1
Lead wire thickness	d1	0.45	±0.1
Bonded lead wire	11	3.0MIN	
Pitch between products	Р	12.7	±0.5
Pitch between perforations	P0	12.7	±0.2
Total pitch for 21 perforations	P0×20	254.0	±1.0
Distance between lead wire	F	5.0	+0.8
Lead wire pitch distance	F1	2.54	+0.4
Displacement of a effections	P1	3.81	±0.3
Displacement of perforations	P2	6.35	±0.3
Displacement of tape	W2	0~0. 5	L

Unit:mm

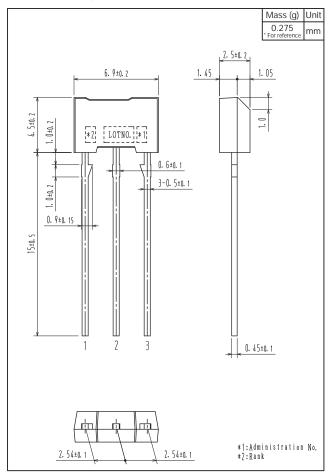
ltem	Symbol	Standard	Tolerance
Tape width	W	18.0	±0.5
Adhesive tape	Wo	6.0	士0.5
Displacement of perforations	W1	9.0	±0.5
Work piece bottom surface position	Н	19.0	+1.0
Work piece upper limit position	H1	23.5	±1.0
Perforations diameter	D0	φ4.0	±0.2
Tape thickness (total thickness)	t	0.6	±0.2
Product inclination	Δс	0	±0.7
Product inclination	Δh	0	±1.0



- · Provide an empty section for about three to five pleas in leading and end portions of the tape.
- · Provide an empty section in the fold-back portion.
- · Provide marking in red to the E-side end of the board.

Outline Drawing

2SC4482T-AN, 2SC4482U-AN



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